

GA102 DT SKU P-BOARD

350W, FH Std PCB, 384b, GDDR6X x8
DP + DP + DP + HDMI/DP

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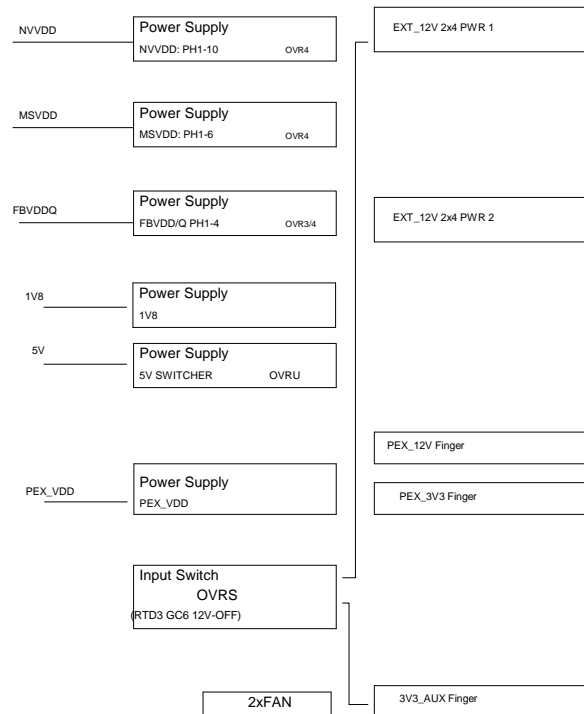
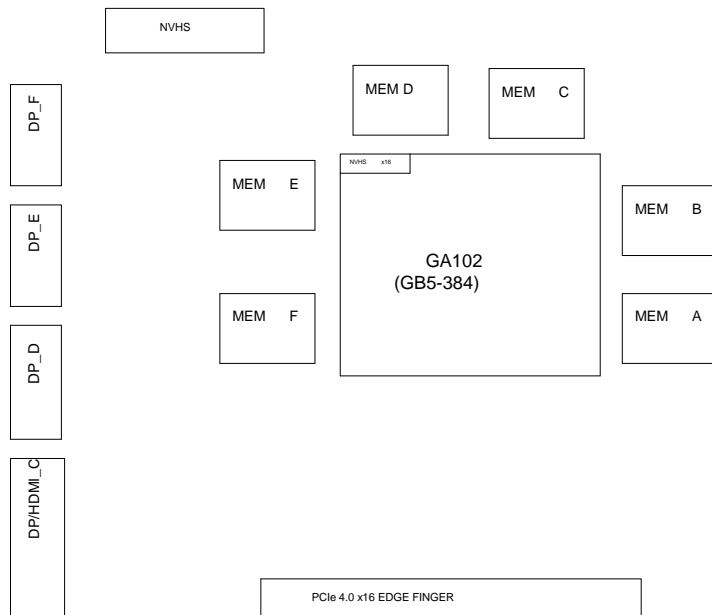
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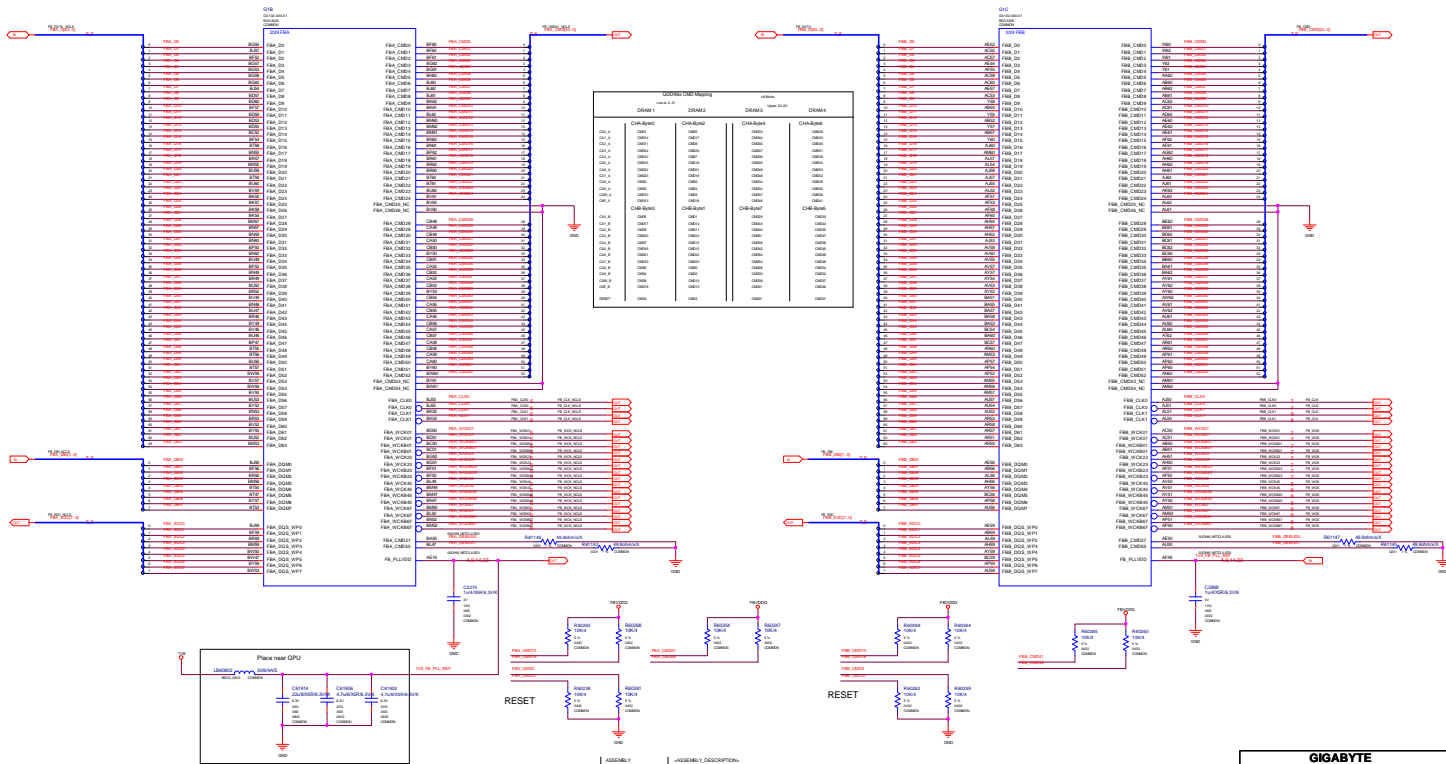
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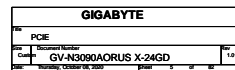
GIGABYTE	
PCIE	
GV-N3090AORUS X-24GD	

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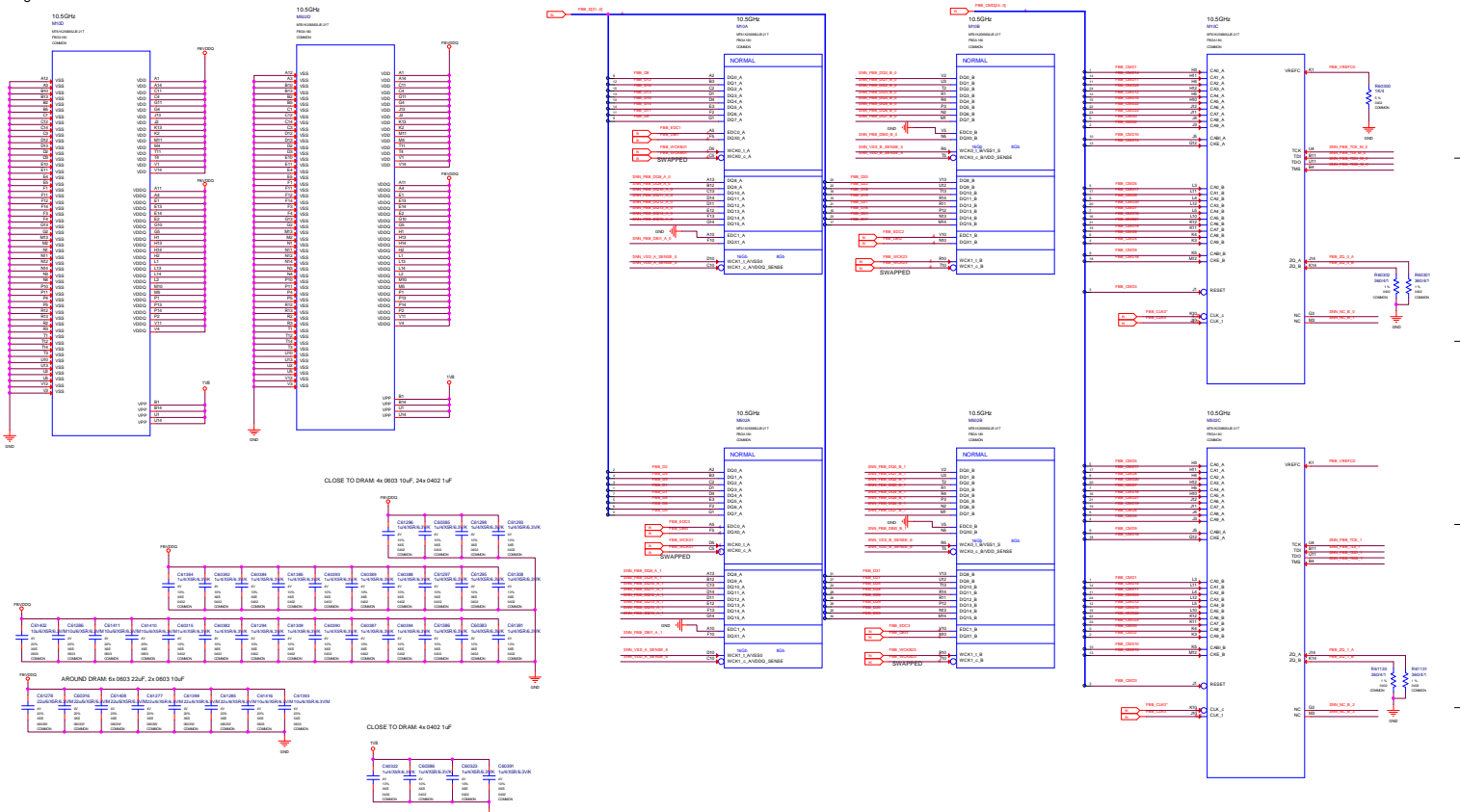
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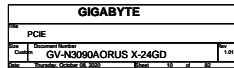


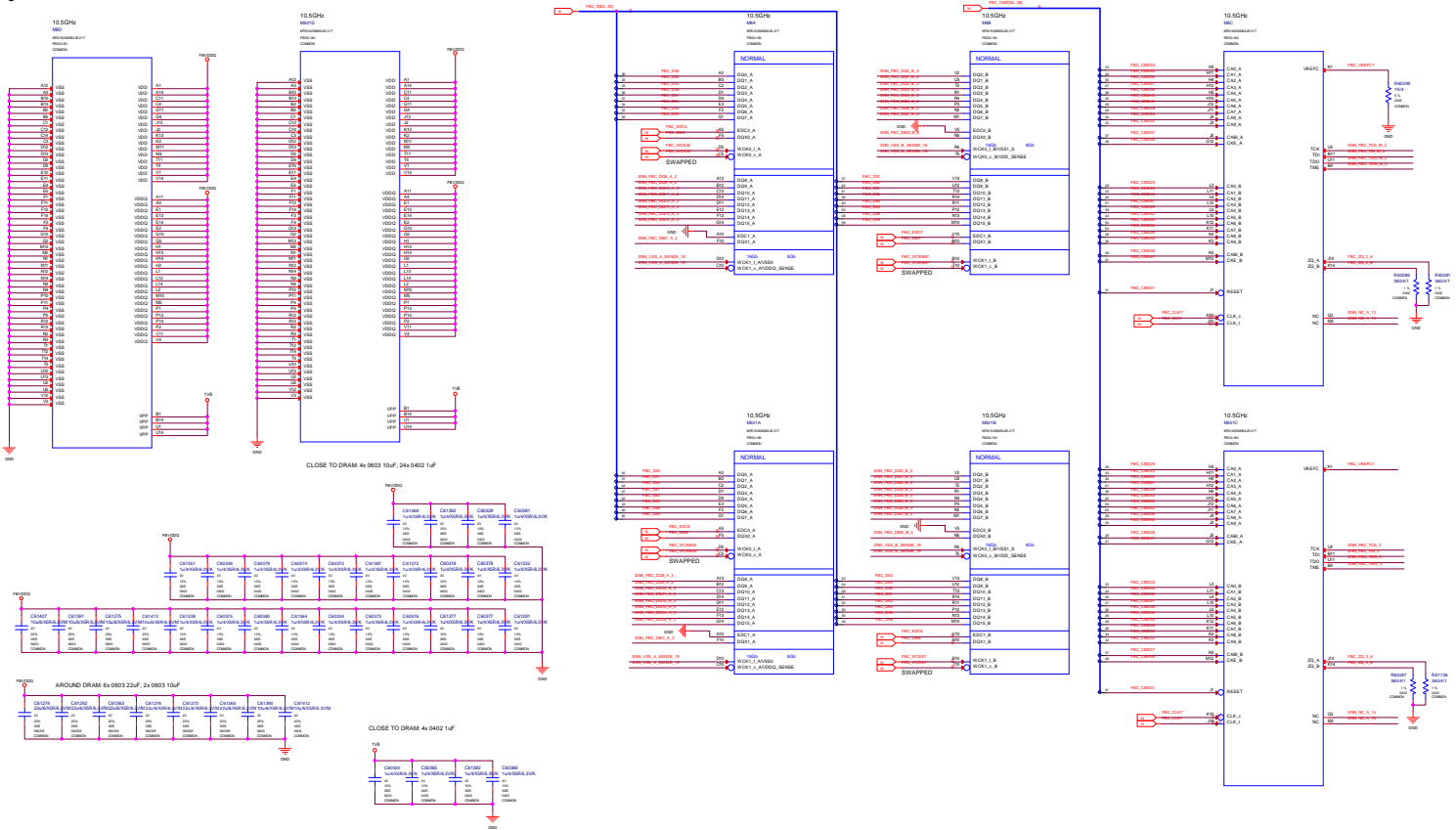


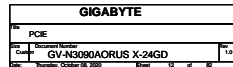




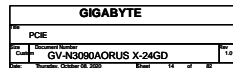


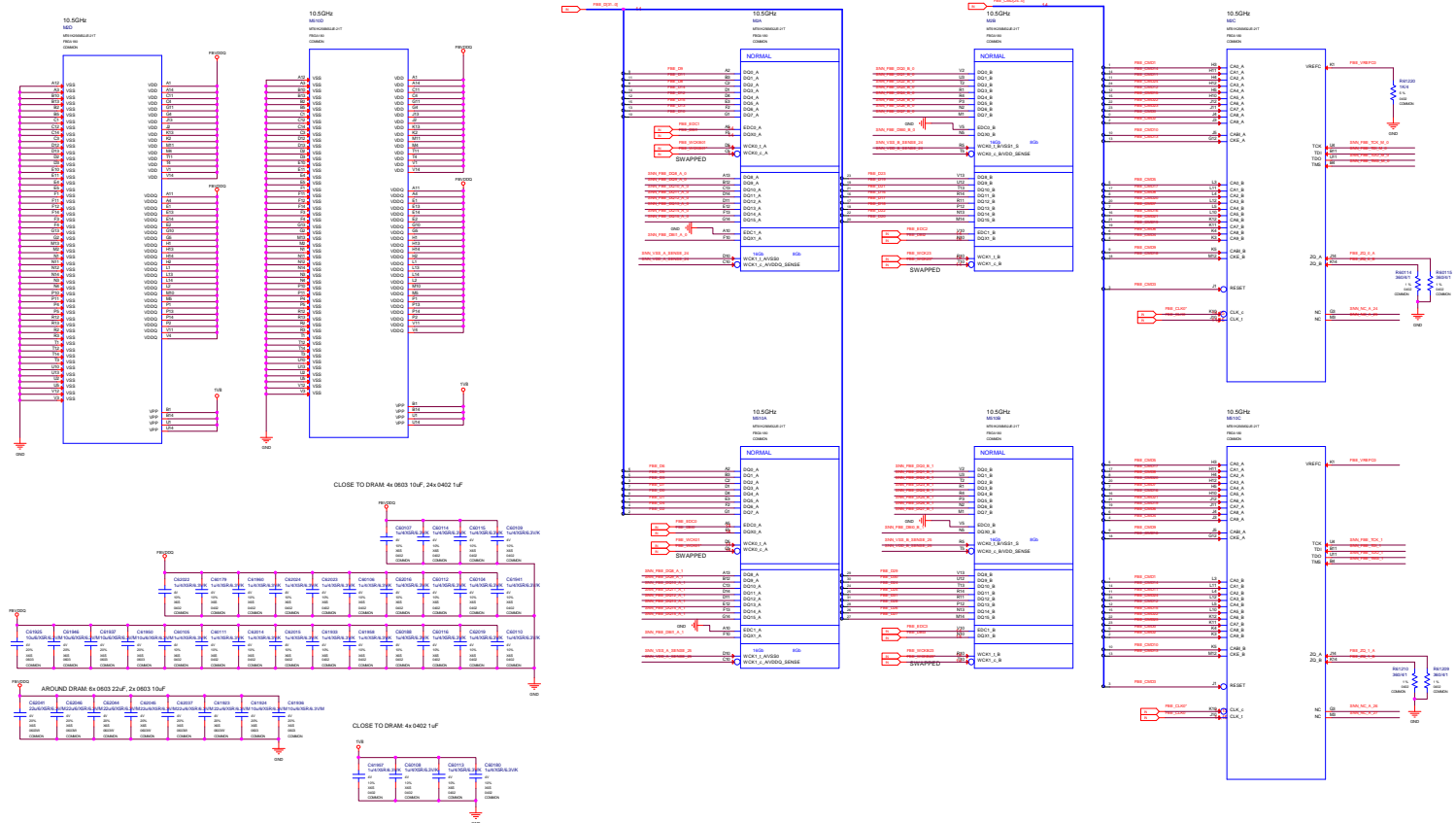


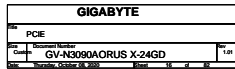
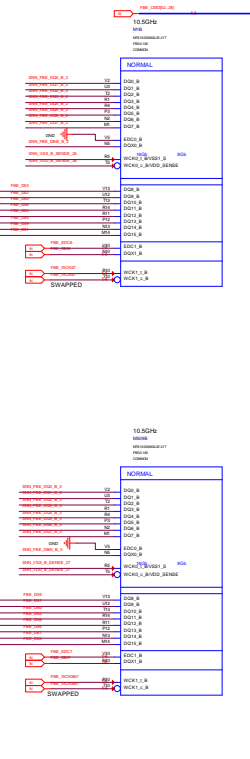


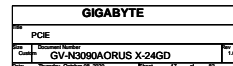


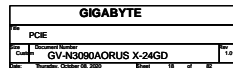




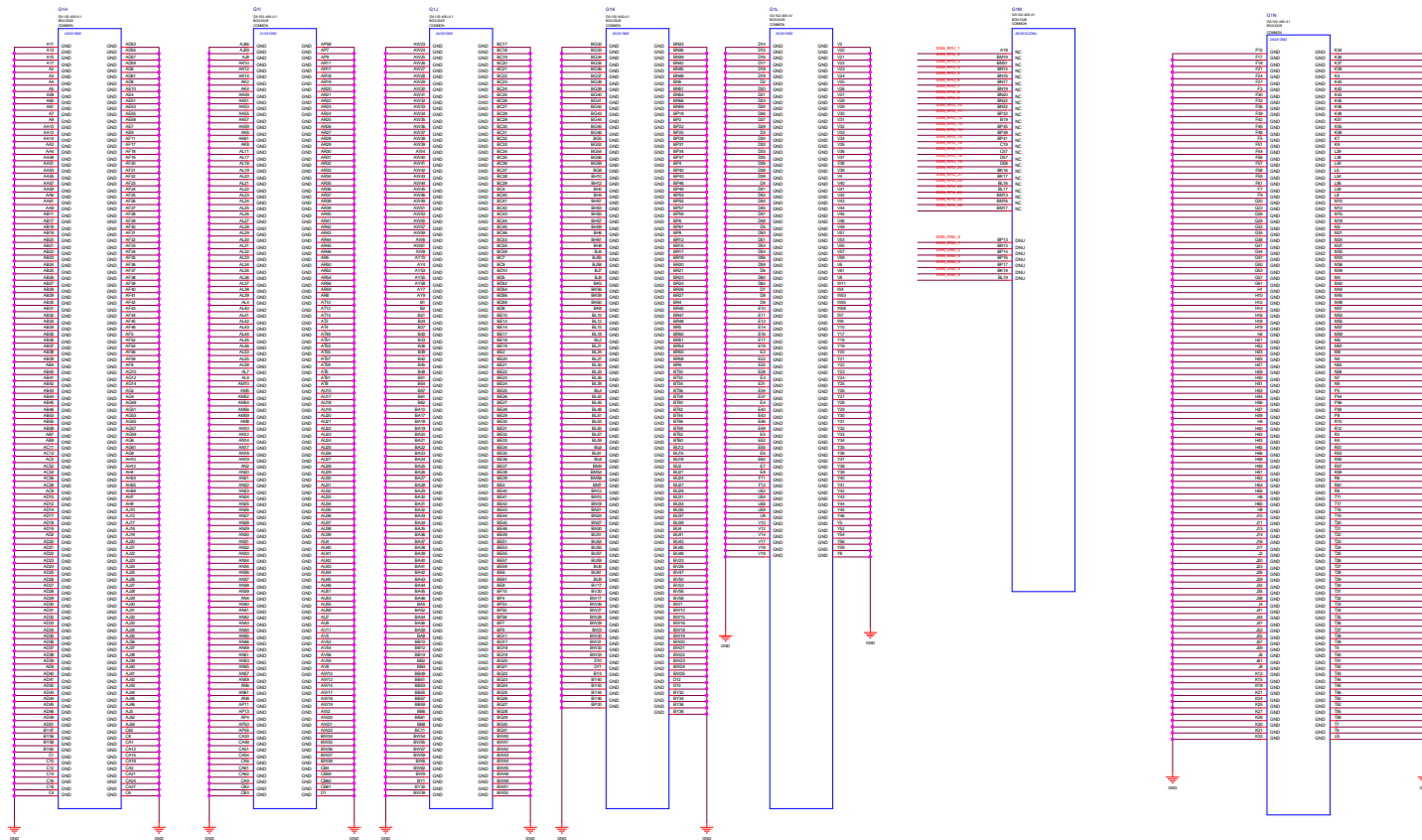








GPU GND, RFUs & RSVD



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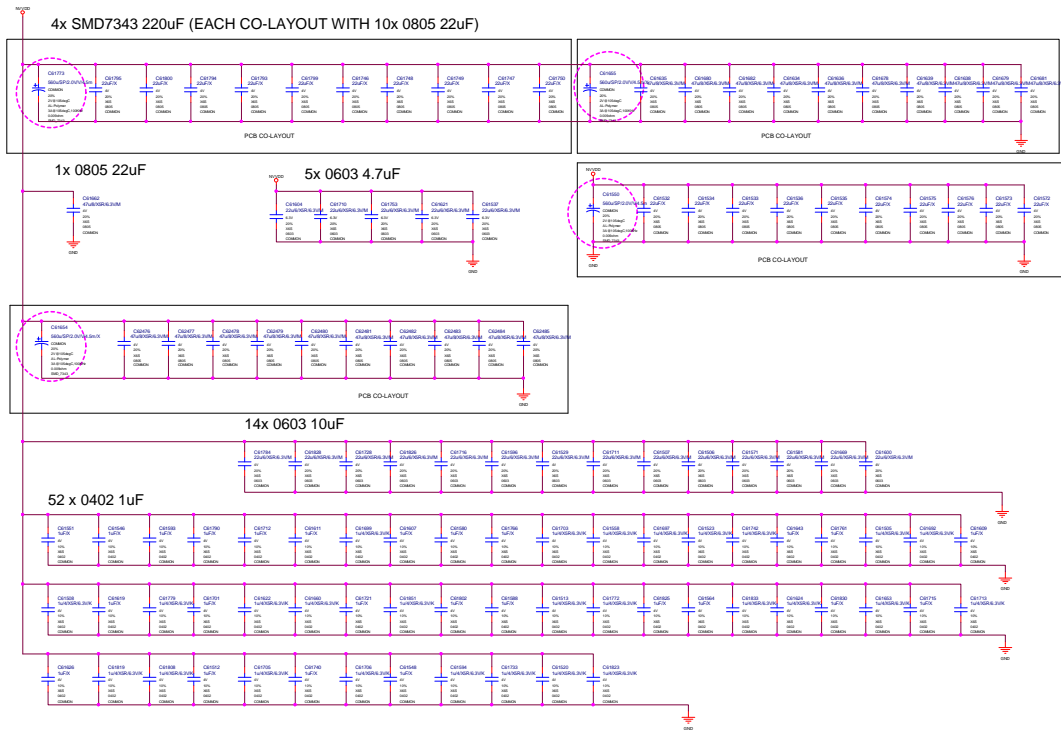
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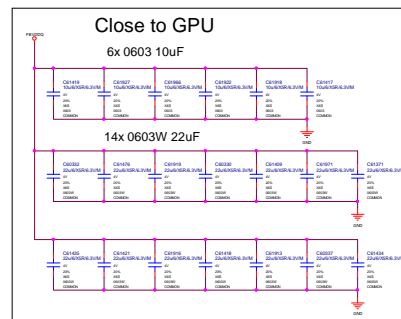
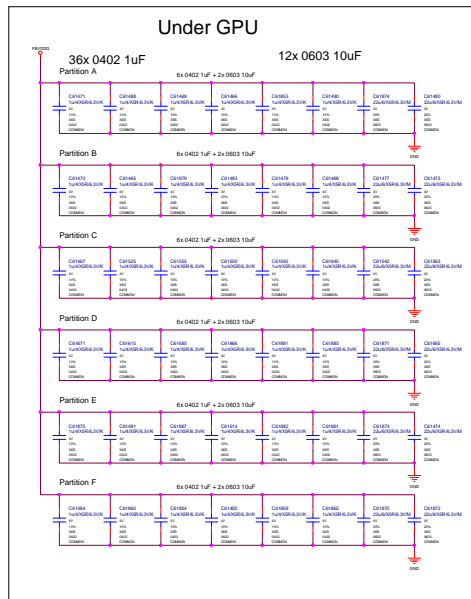
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NVVDD

UNDER GPU

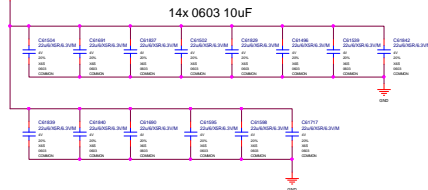


FBVDDQ

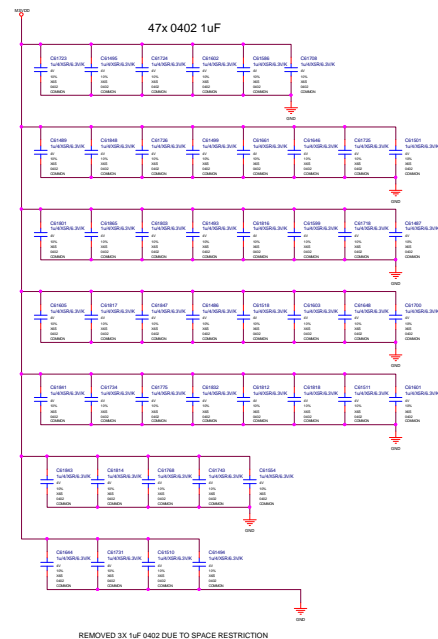
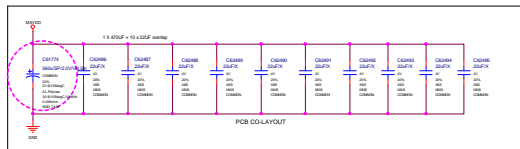
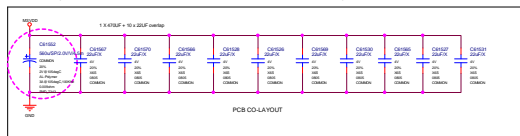


MSVDD

UNDER GPU



2x SMD7343 220uF (EACH CO-LAYOUT WITH 10x 22uF 0805)



GIGABYTE

PCIE	
Chipset	GV-N3090AORUS X-24GD
Rev	1.00

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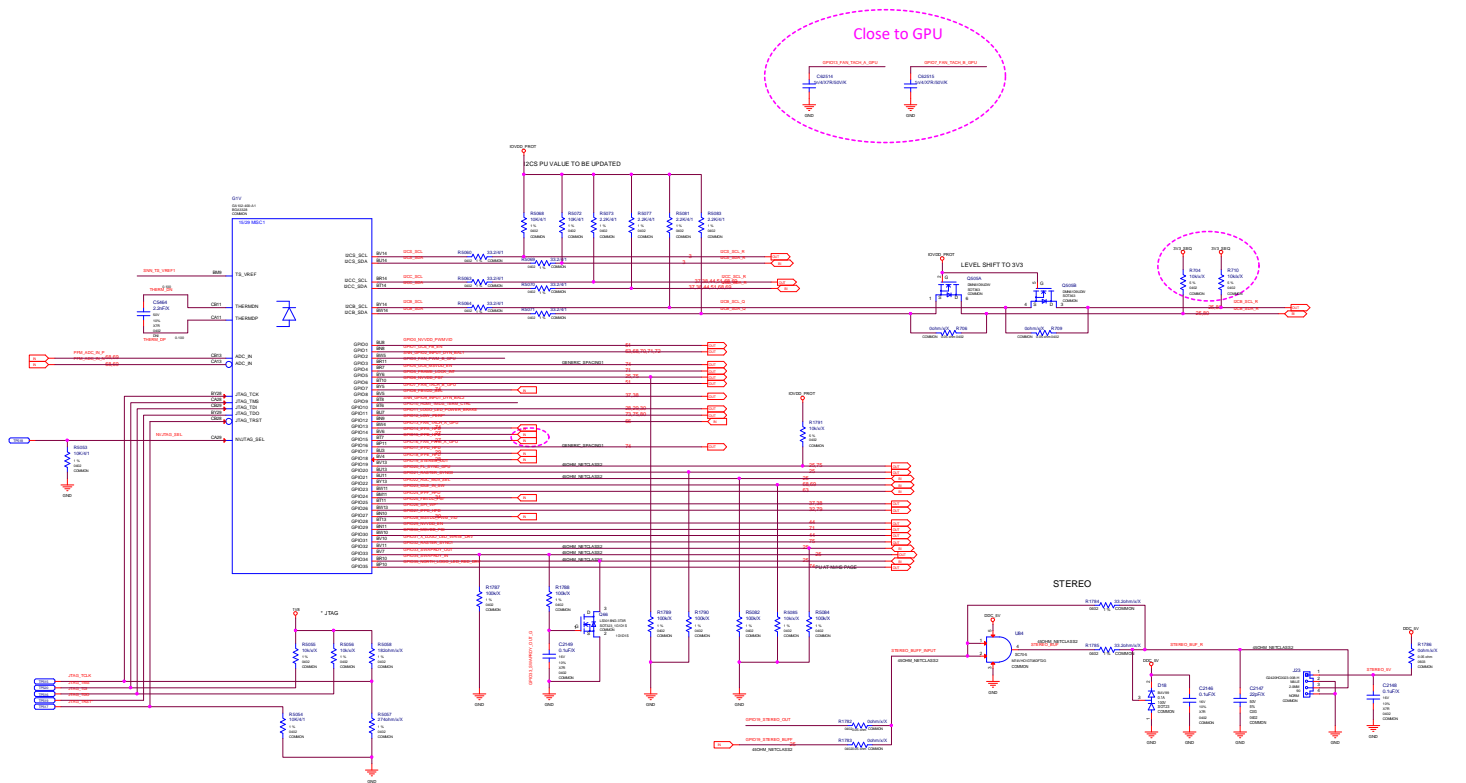
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PAGE DETAIL

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GIGABYTE	
PCIE	
GV-N3090AORUS X-24GD	
NVIDIA GeForce RTX 3090	

MISC1: Fan, Thermal, JTAG, GPIO, STEREO



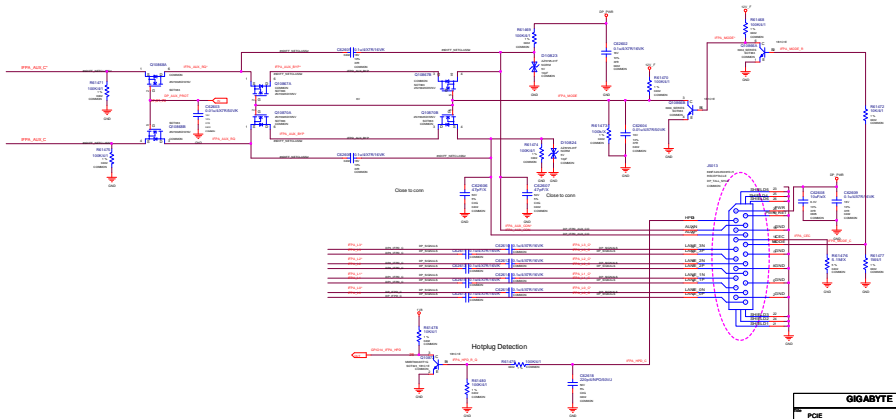
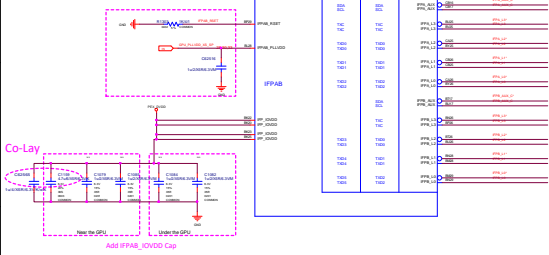
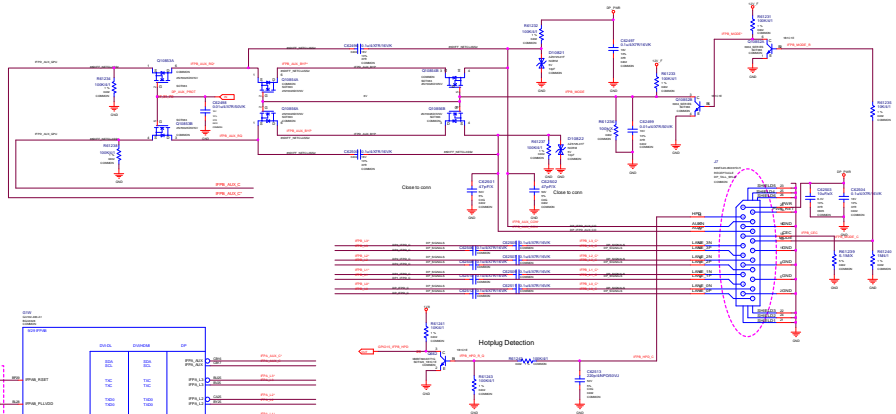
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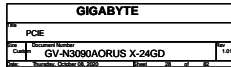
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MSD THERMAL TAG GPC

IFPA, IFPB For DP x2

Co-Lay

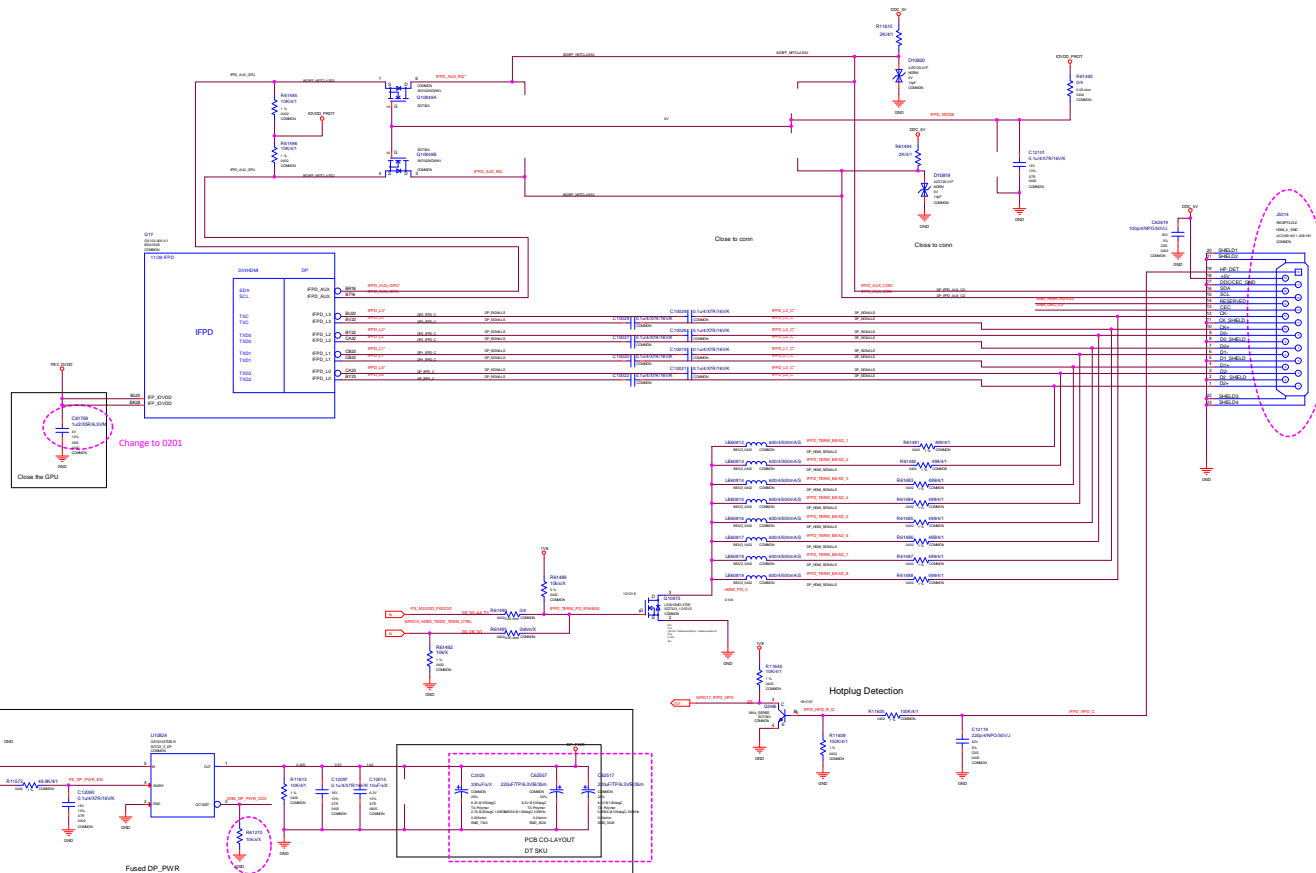


Change to HDMI



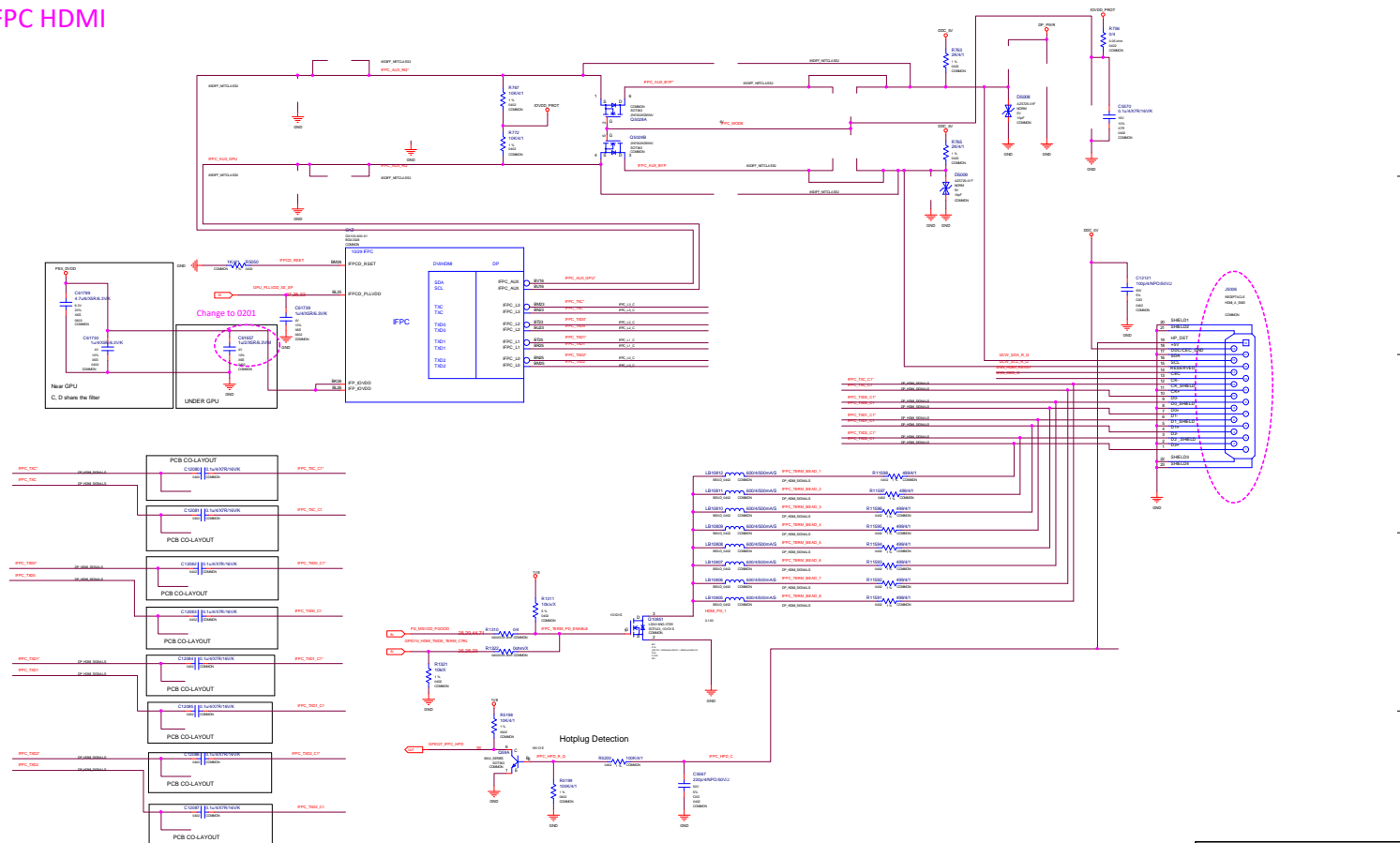
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IFPD HDMI



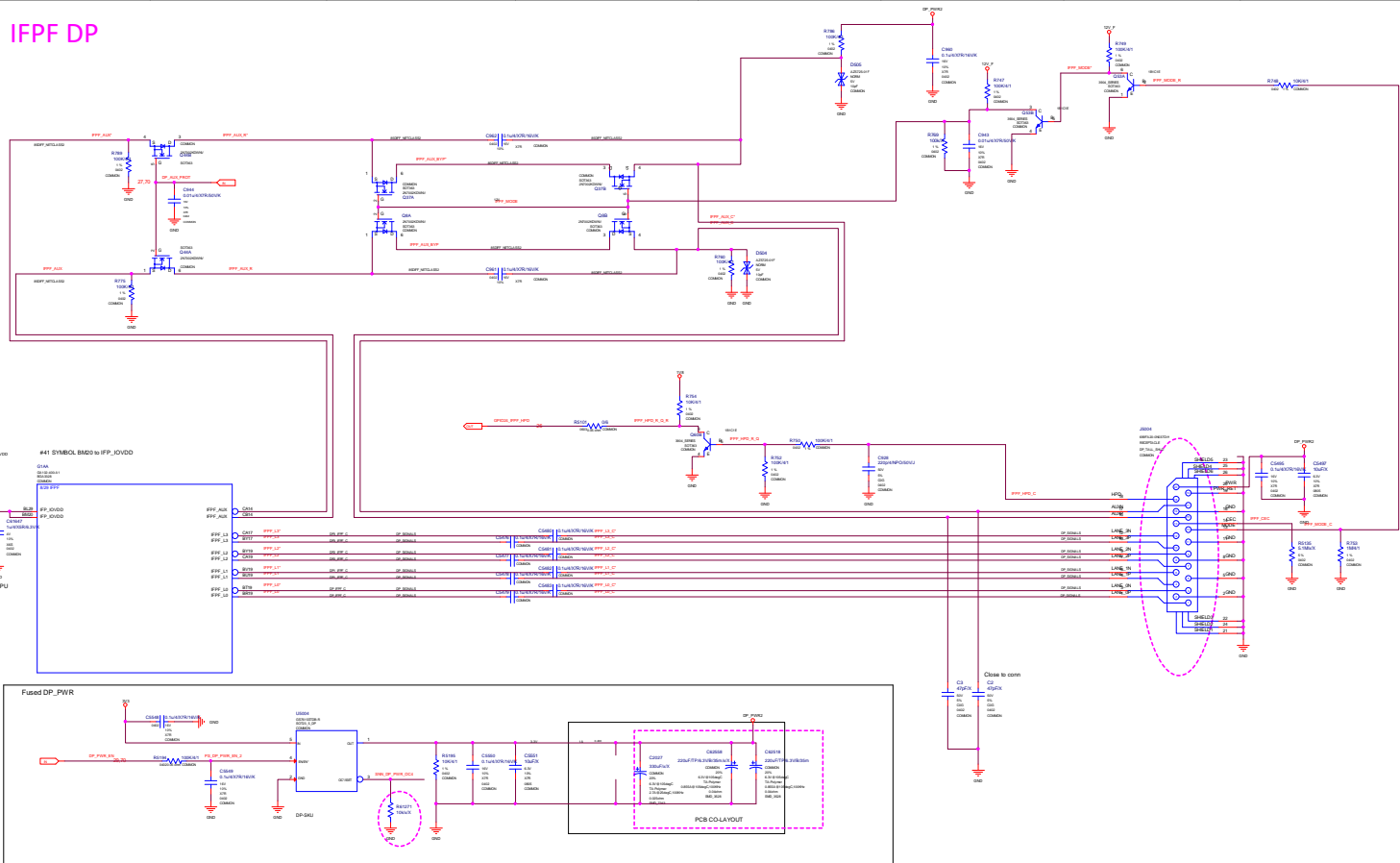
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IPMC HDMI



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IFPF DP



MISC: ROM, Straps

STRAP2	STRAP1	STRAP0	RAMCFG[4:0]	
L	L	L	00000	RAMCFG MICRON 8Gb 19Gbps
H	H	L	00010	RAMCFG TBD
H	L	L	00100	RAMCFG TBD
H	L	H	00101	RAMCFG TBD
H	L	H	00101	RAMCFG TBD
H	L	H	00101	RAMCFG MICRON 8Gb 21Gbps

DEFAULT

H=High :Tied to 1.8V
M=Middle:Tied to 0.9V
L=Low :Tied to 0V

ROM_SO	ROM_SI	ROM_SCLK	SMARTFANPULSEFS_OVERT	1:ENABLE 0:DISABLE
H	H	H	0111	FS_OVERT ENABLE
L	L	L	0000	FS_OVERT DISABLE

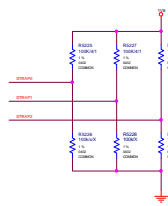
DEFAULT

STRAP5	STRAP4	STRAP3	SMB_ALT_ADDR	DEVID_SEL	PCIE_CFG	VGA_DEVICE
M	H	L	1	1	1	1
M	H	L	1	H	1	0
M	L	H	1	1	0	1
M	L	L	1	1	0	0
L	H	M	1	0	1	1
L	M	H	1	0	1	0
L	M	L	1	0	0	1
L	L	M	1	0	0	0
H	H	H	0	1	1	1
H	H	L	0	1	1	0
H	L	H	0	1	0	1
H	L	L	0	1	0	0
L	H	H	0	0	1	1
L	H	L	0	0	1	0
L	L	H	0	0	0	1
L	L	L	0	0	0	0

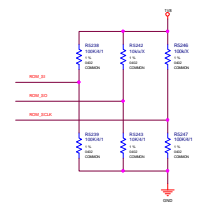
Default

1:SMB_ALT_ADDR ENABLE
0:SMB_ALT_ADDR DISABLE
1:DEVID_SEL REBRAND
0:DEVID_SEL ORIGINAL
1:PCIE_CFG LOW POWER
0:PCIE_CFG HIGH POWER
1:VGA_DEVICE ENABLE
0:VGA_DEVICE DISABLE

GROUP0

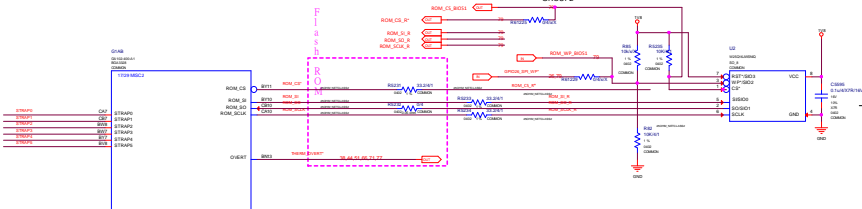
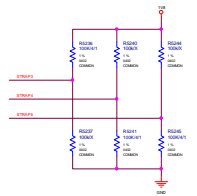


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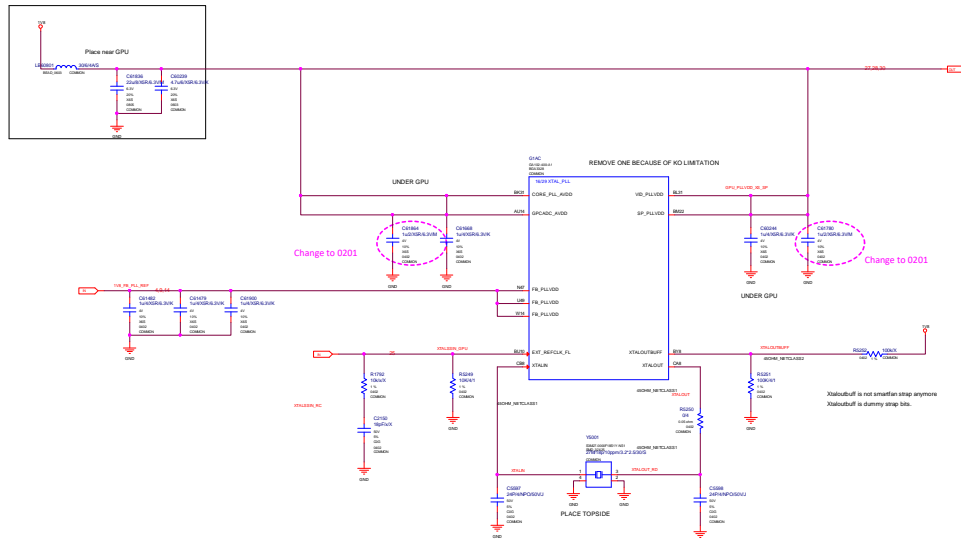


Modify

GROUP2



MISC: XTAL. PLL



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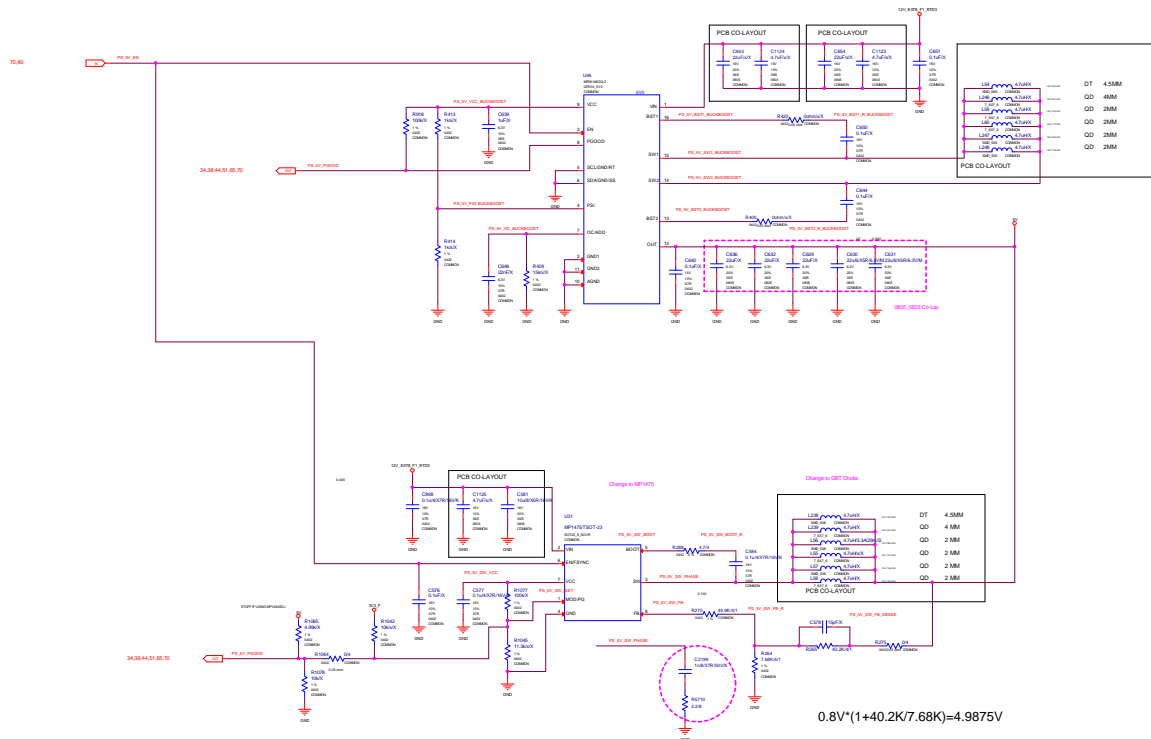
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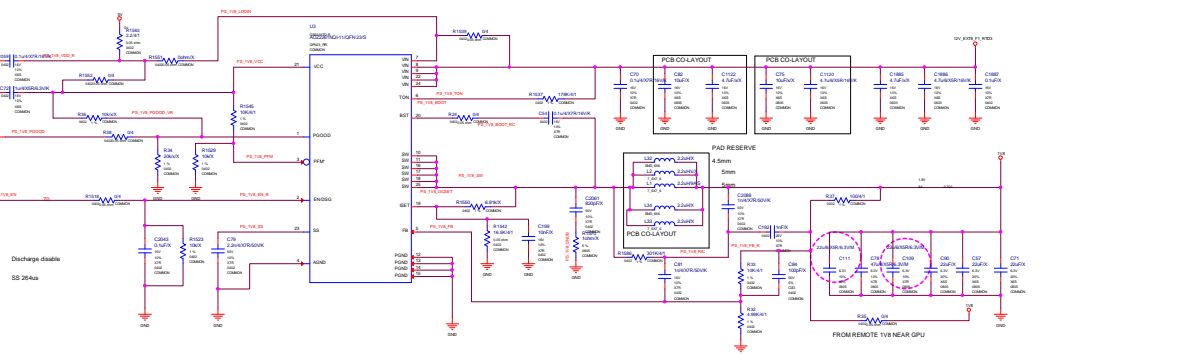
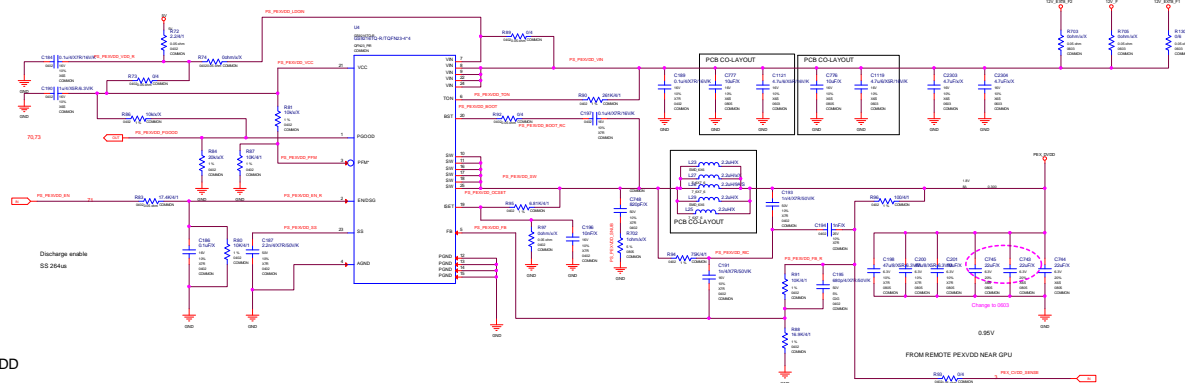
GIGABYTE

PCIE

Document Number	GV-N3090AORUS X-24GD
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101


$$0.8V \cdot (1 + 40.2K/7.68K) = 4.9875V$$



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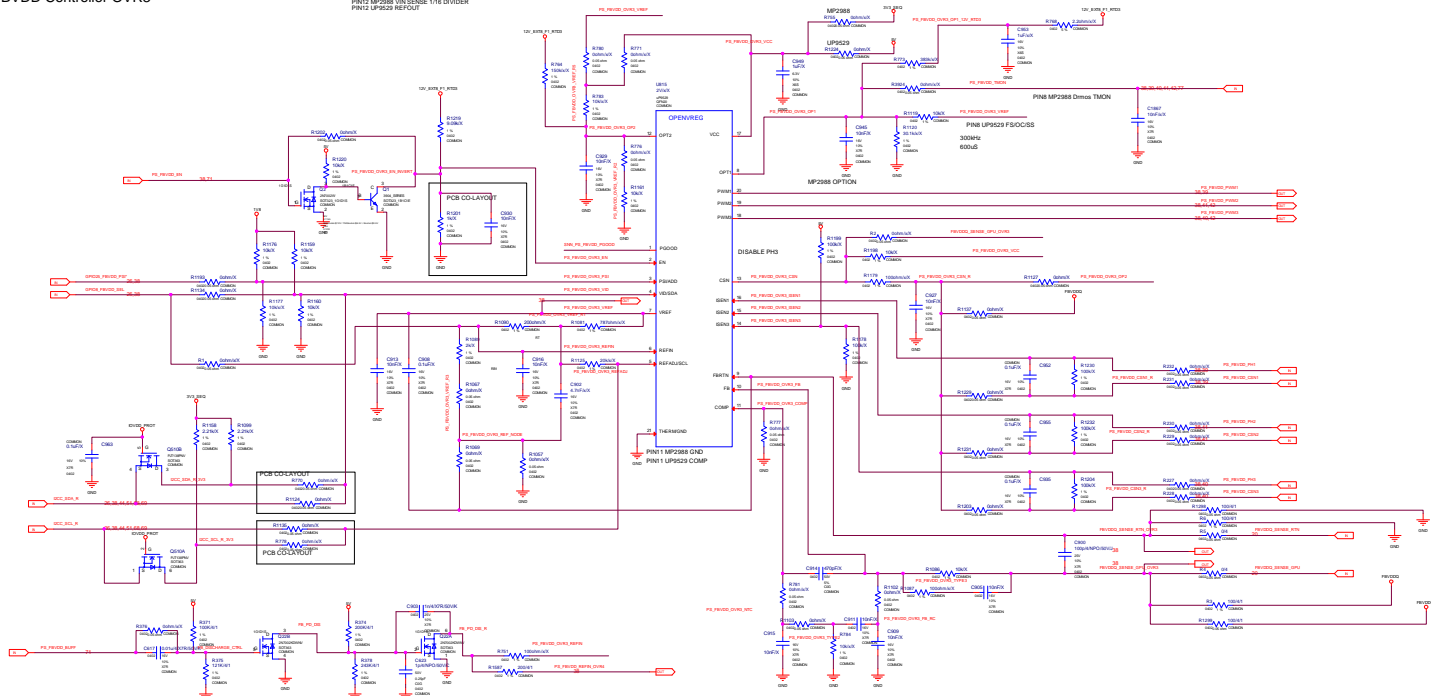
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GIGABYTE	
PCIE	
GV-N3090AORUS X-24GD	
Rev 1.01	

PS: FBVDD Controller OVR3

PIN12 MP2088 VIN SENSE 1/16 DIVIDER
PIN12 UP2529 REFOUT



FOR DT SKU

ASSEMBLY
PAGE 0270

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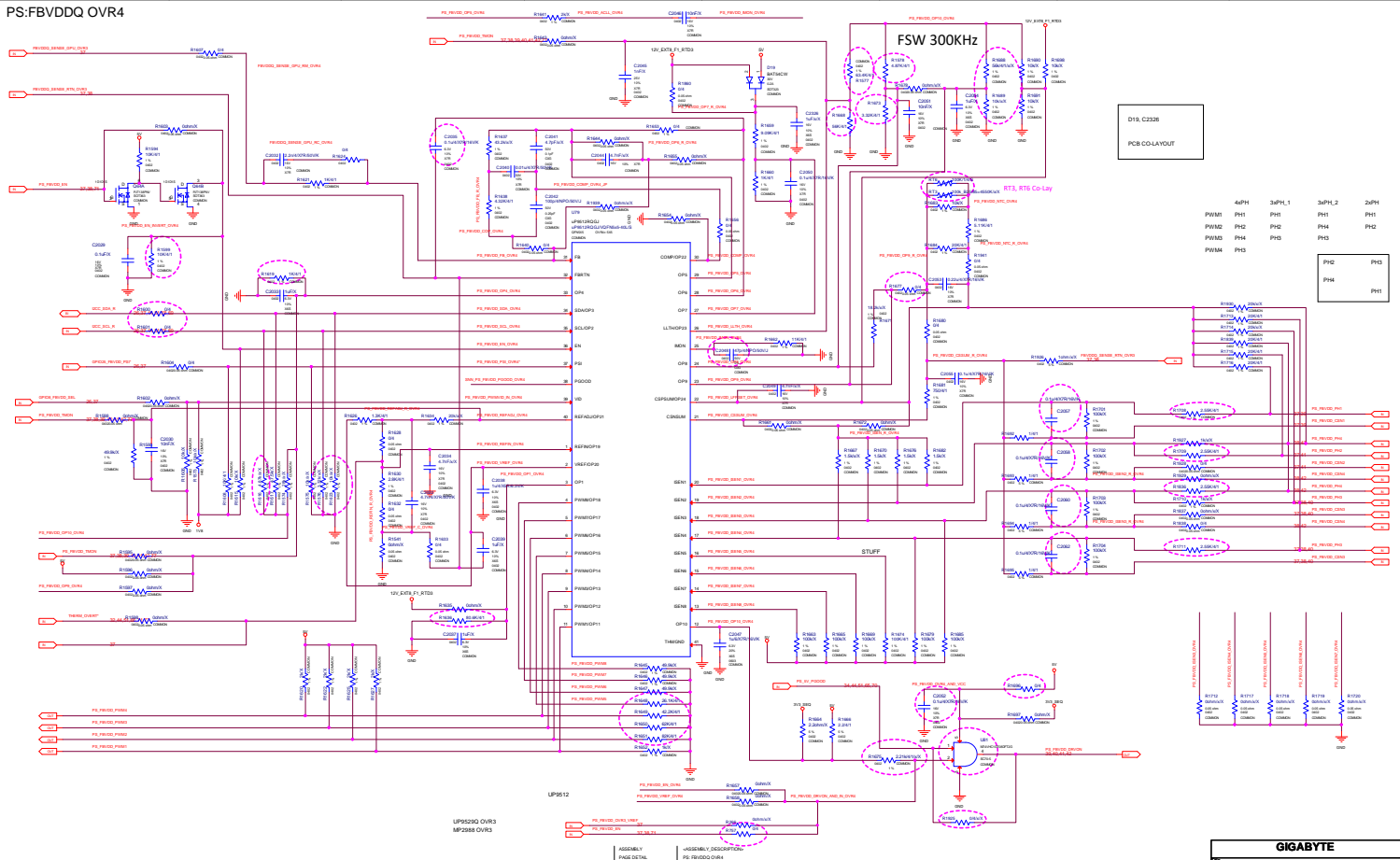
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PS:FBVDDQ OVR4



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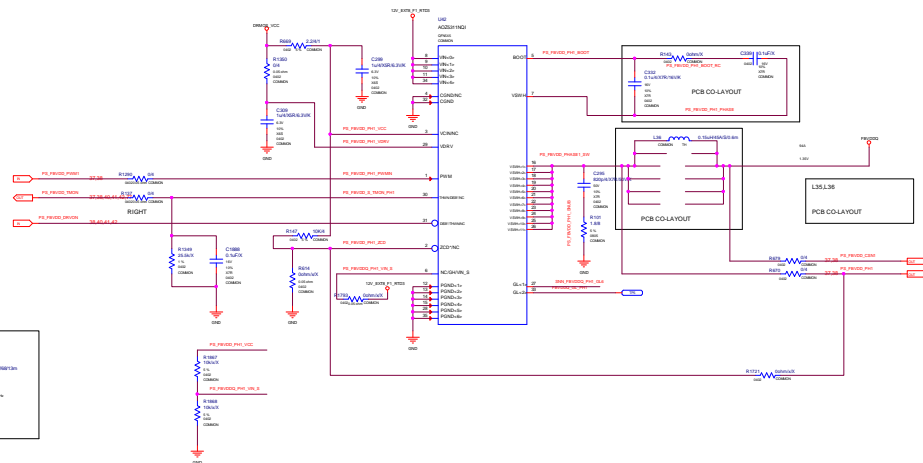
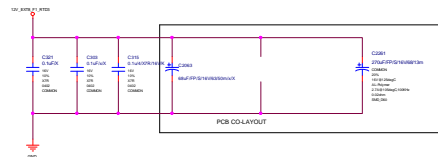
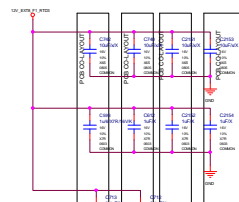
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PCI

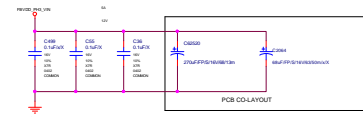
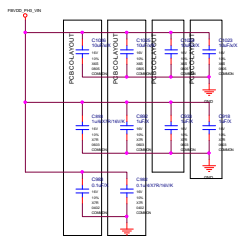
Document Number	GV-N3090AORUS X-24GD
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14

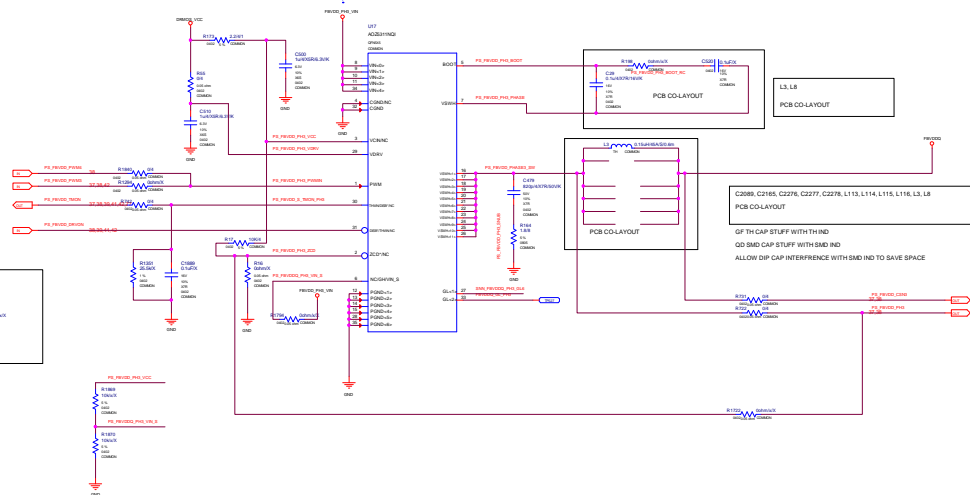
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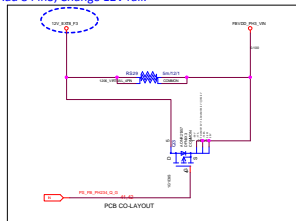
PS: FBVDD PH3



Add 8 Pin3, Change 12V rail.



Add 8 Pin3, Change 12V rail.



FBVDD PH3 INPUT ANTI-BACKDRIVE

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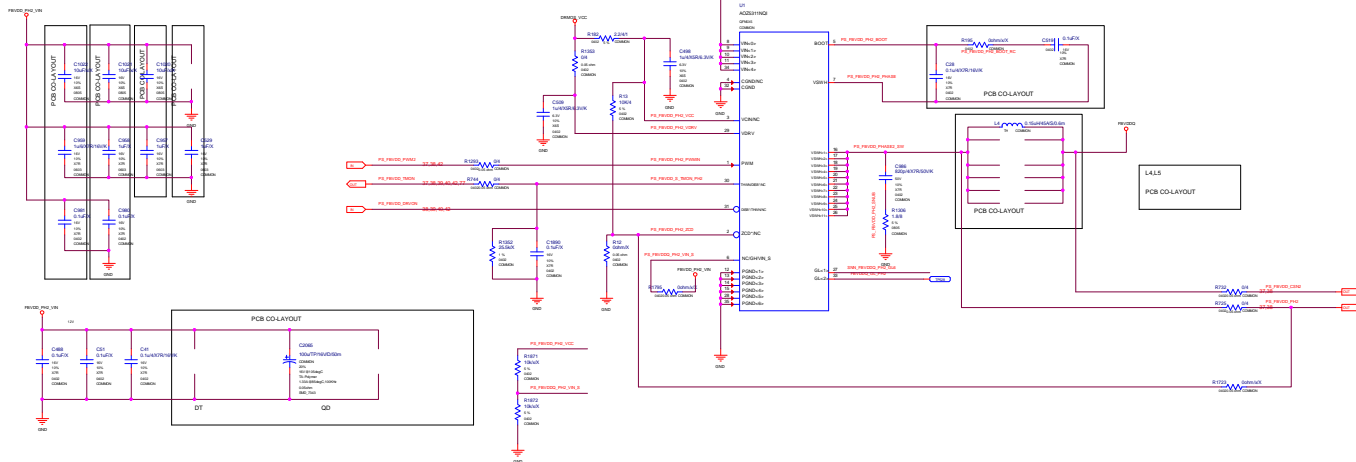
ASSEMBLY
PAGE DETAIL

ASSEMBLY DESCRIPTION
PS: FBVDD PH3

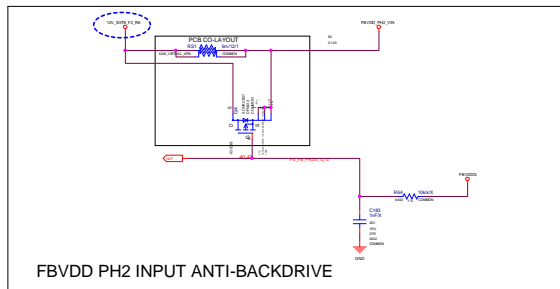
GIGABYTE	
PCB	GV-N3090AORUS X-24GD
Model: GV-N3090AORUS X-24GD	Rev: 1.00

PS: FBVDD PH2

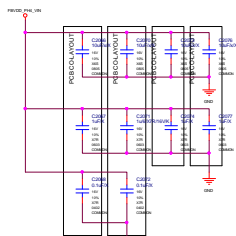
Add 8 Pin3, Change 12V rail.



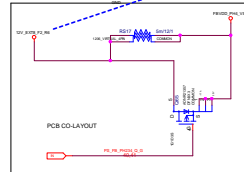
Add 8 Pin3, Change 12V rail.



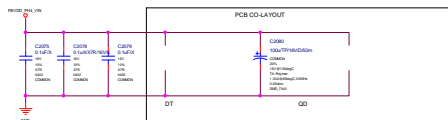
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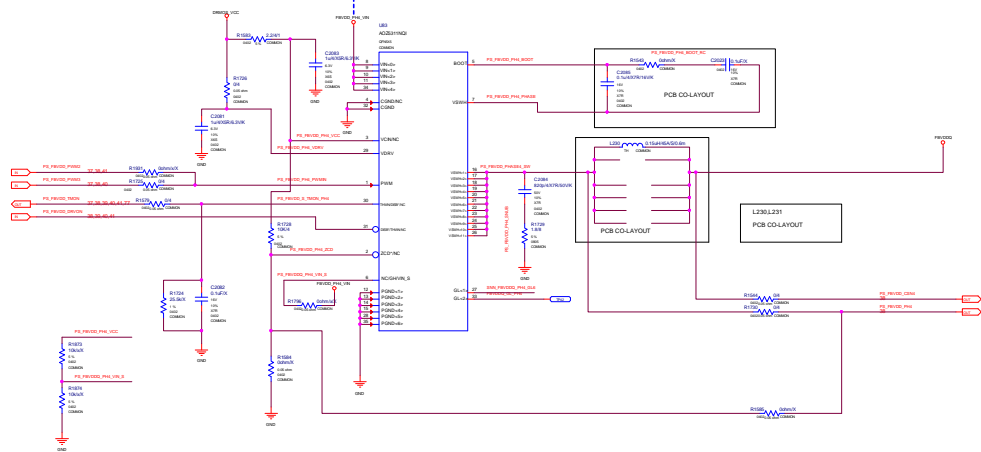
Add 8 Pin3, Change 12V rail.



FBVDD PH3 INPUT ANTI-BACKDRIVE

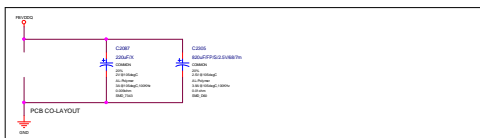


Add 8 Pin3, Change 12V rail.



REFER TO PCB FOR BOM STUFFING

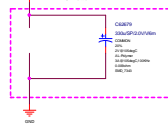
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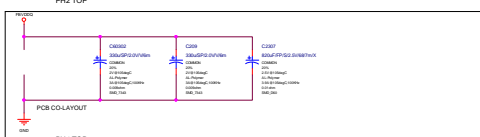
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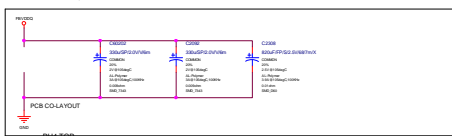
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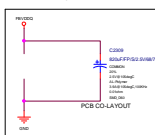
PH2 TOP



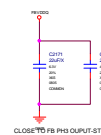
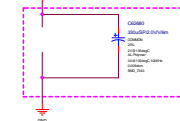
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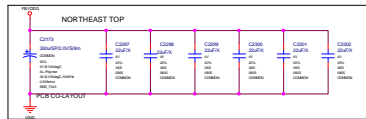
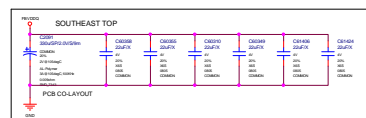
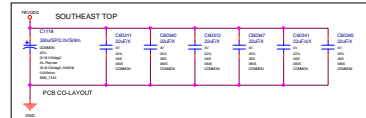
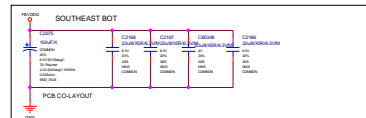
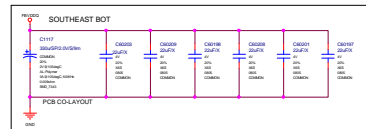
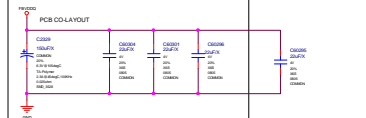
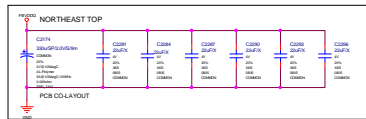
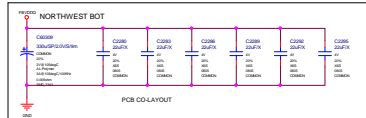
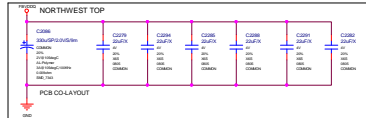
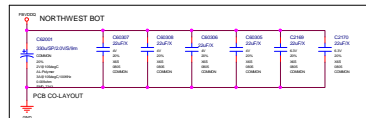
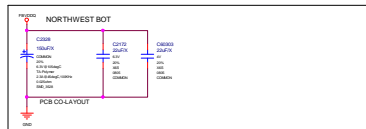
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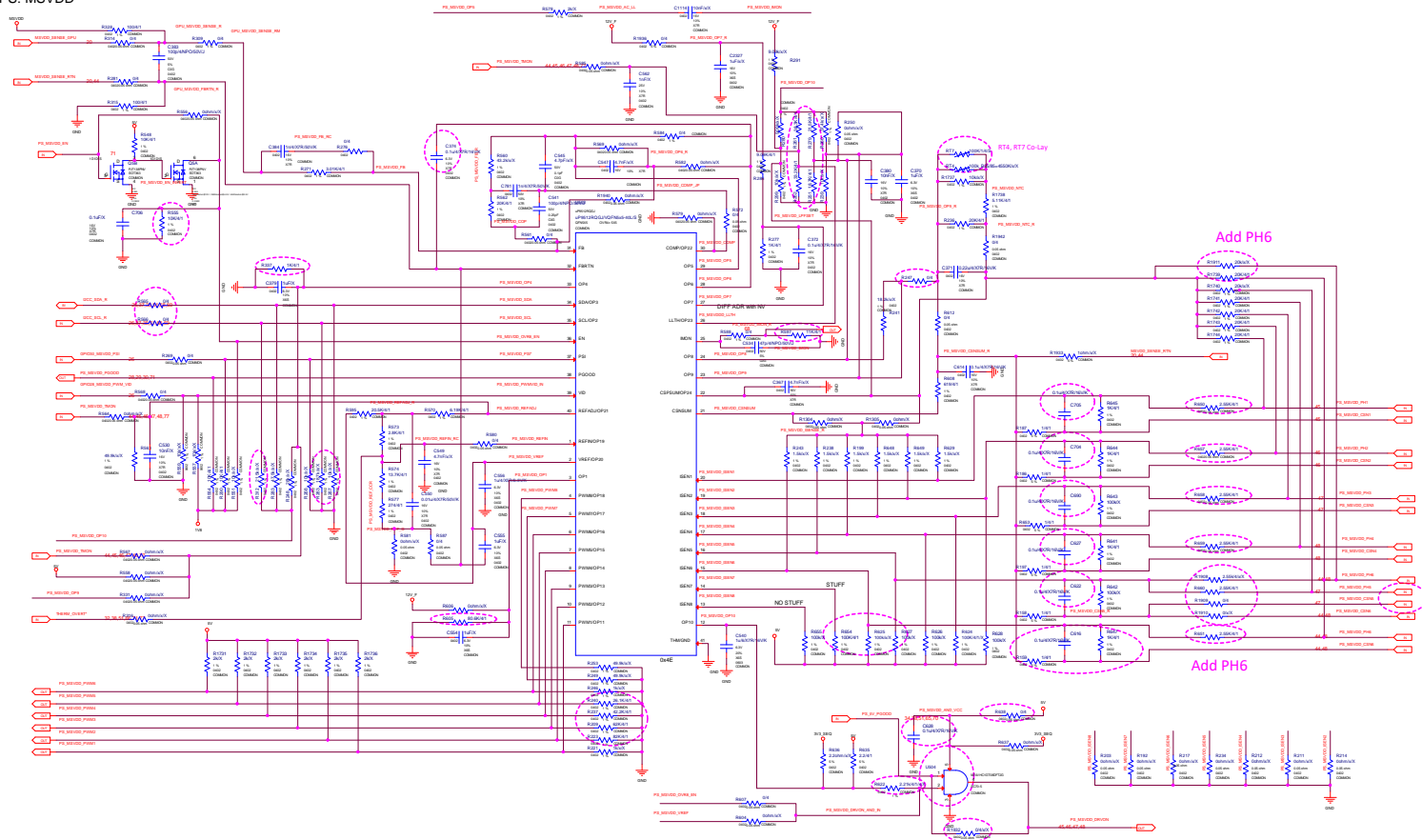


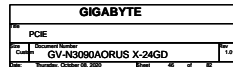
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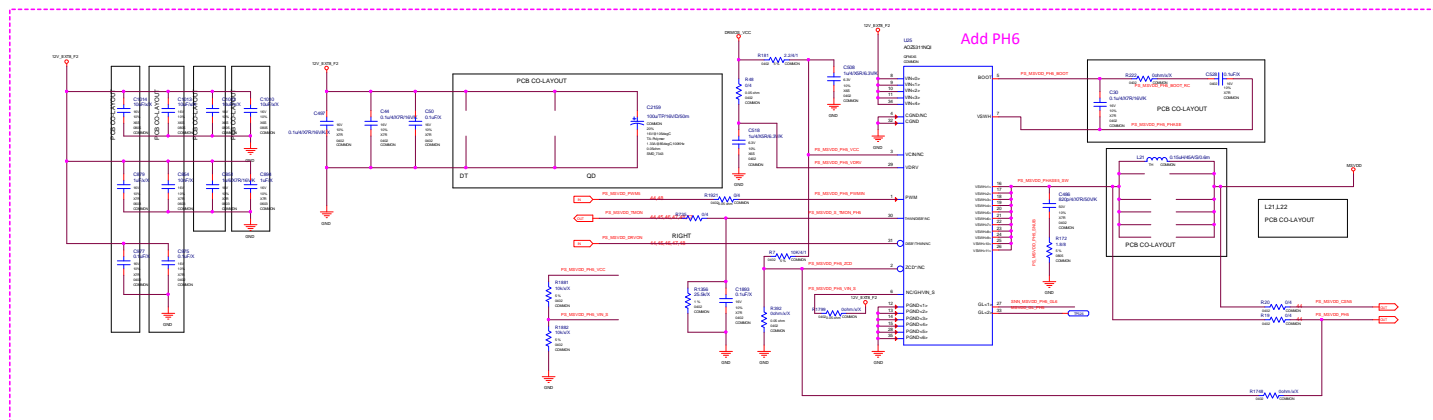
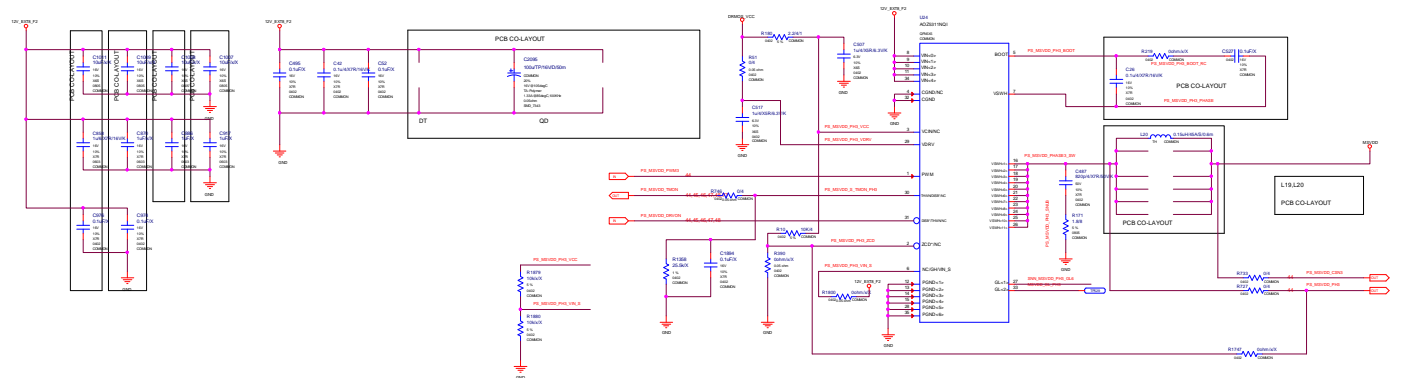
CLOSE TO PH3 PH4 OUTPUT STUFF







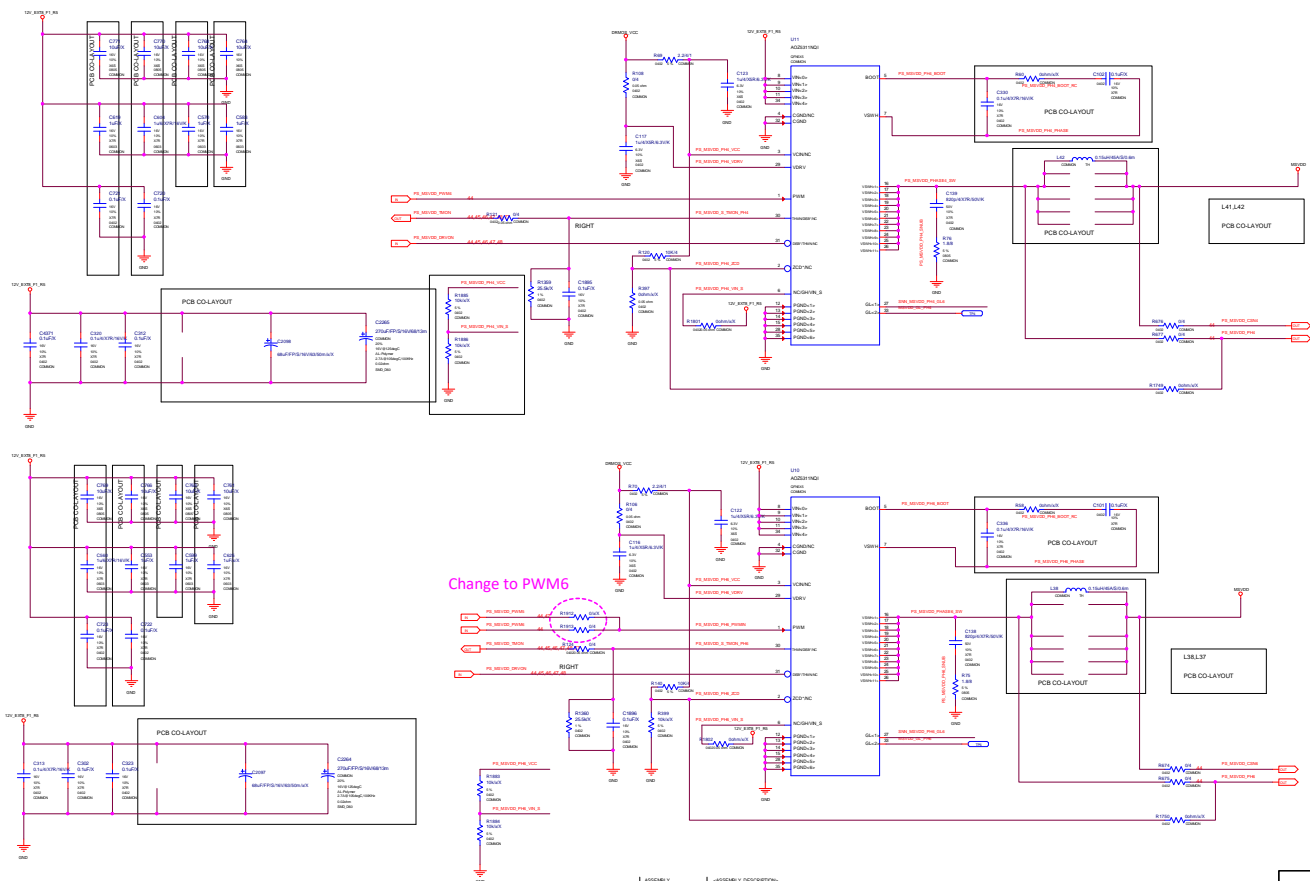
PS:MSVDD Phase 3, 5



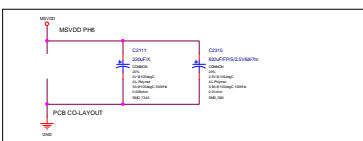
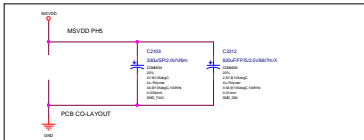
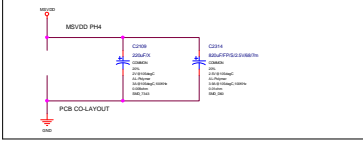
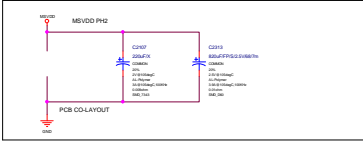
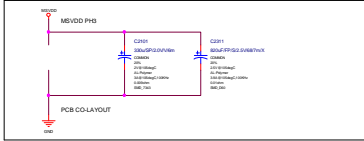
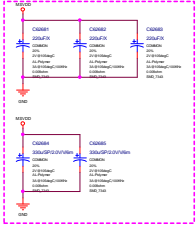
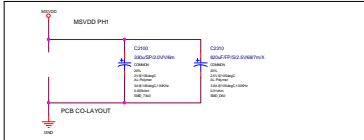
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ASSEMBLY
PAGE DETAIL

ASSEMBLY DESCRIPTION
PS: NEWOD PHS and PHS



REFER TO PCB FOR BOM STUFFING

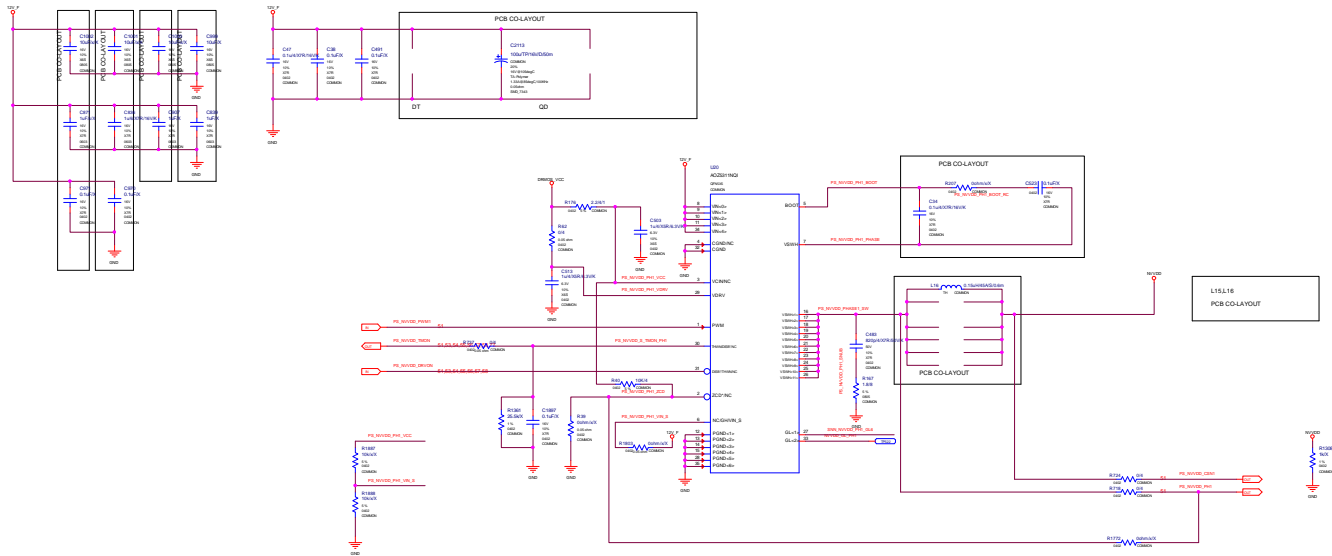


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PS: NVVDD Phase 1

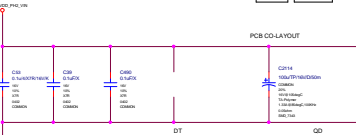
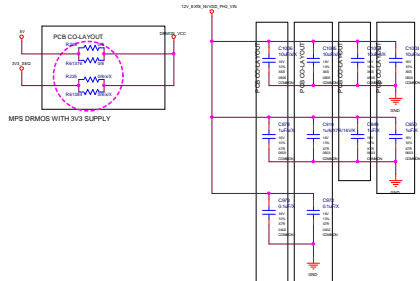


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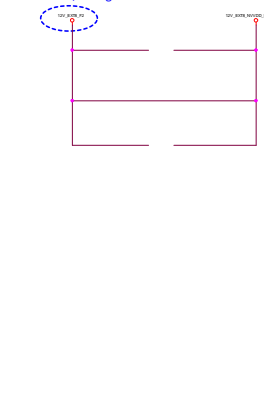
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PAGE DETAIL

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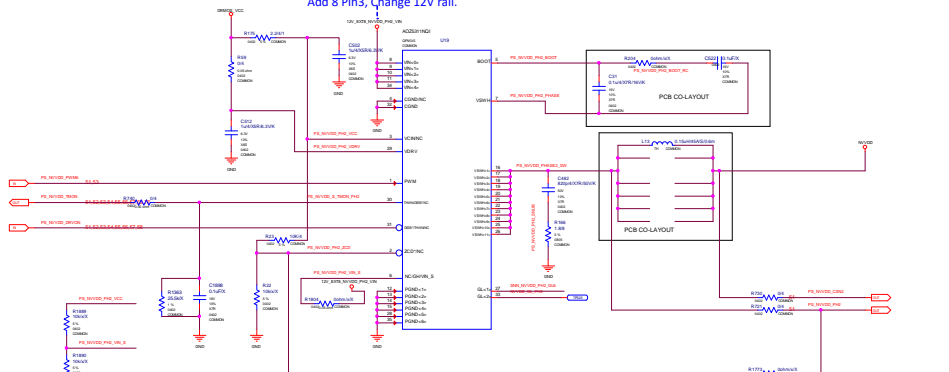
PS: NVDD Phase 2 (PWM6)



Add 8 Pin3, Change 12V rail.

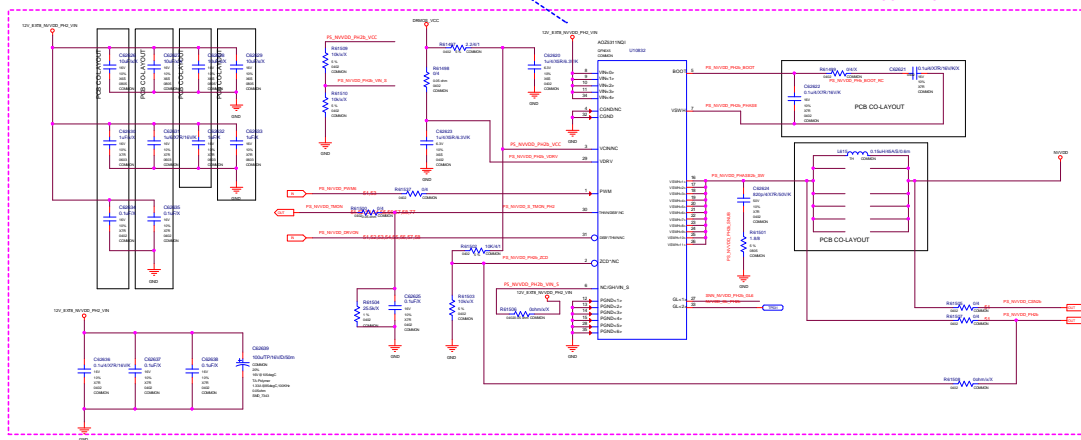


Add 8 Pin3, Change 12V rail.



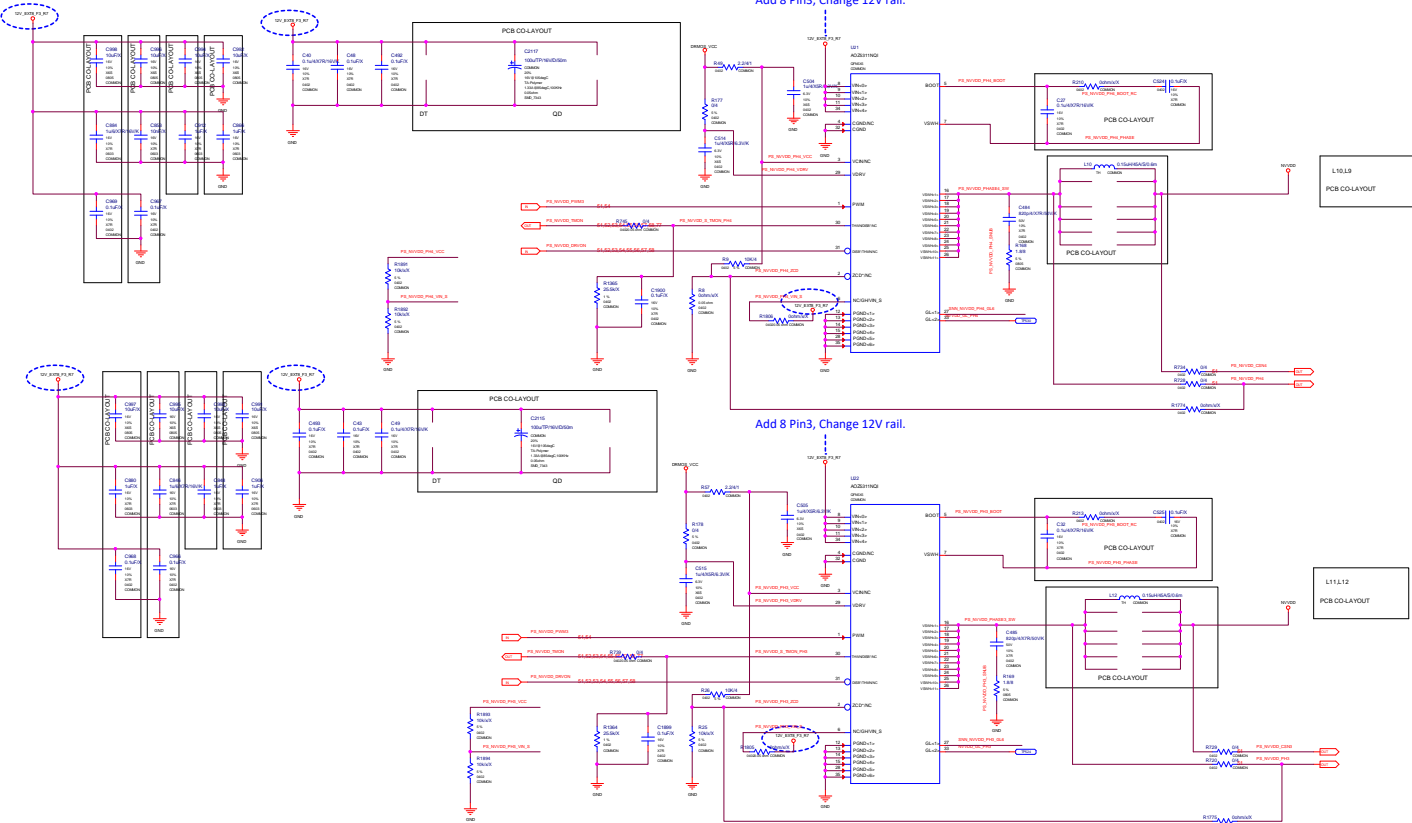
Add 8 Pin3, Change 12V rail.

Add PH2b



PS: NVVDD Phase 3 (PWM3), PHASE4 (PWM3)

Add 8 Pin3, Change 12V rail



Add 8 Pin3, Change 12V rail

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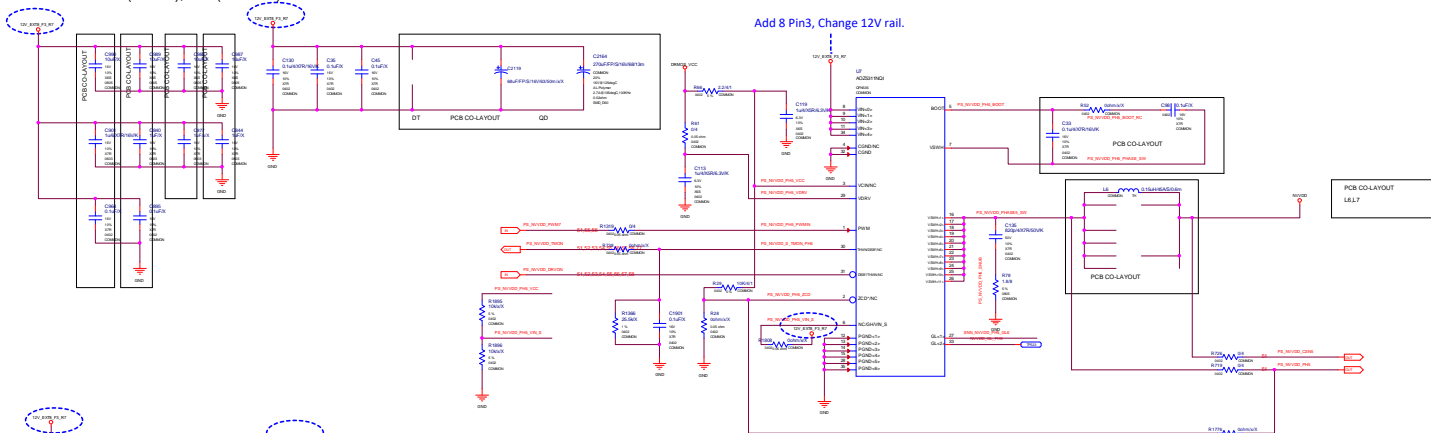
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PS: NAOO P40 (P4M) and P4H (P4M)

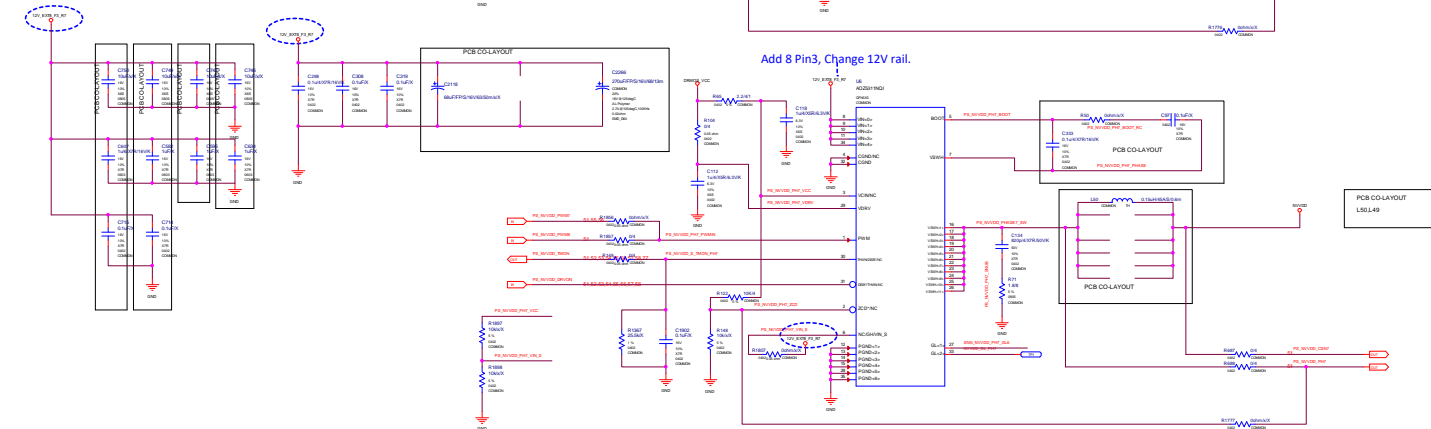
GIGABYTE

BCI

PS: NVVDD PH 5(PWM4), PH7(PWM8/4)



Add 8 Pin3, Change 12V rail.



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ASSEMBLY
PAGE DETAIL

PS: N400 P46 (P4M7) and P47 (P4M8)

GIGABYTE

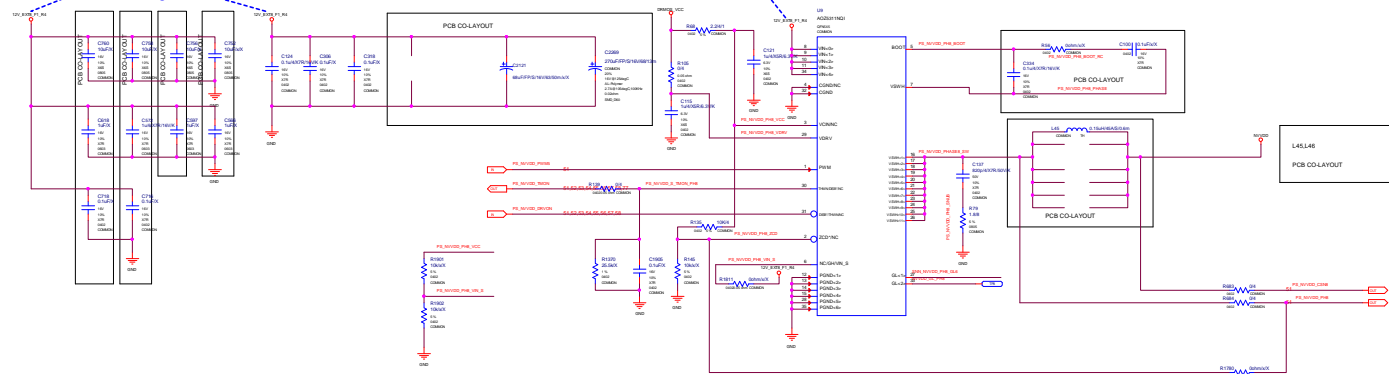
PCI

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➤ Add 8 Pin3, Change 12V rail

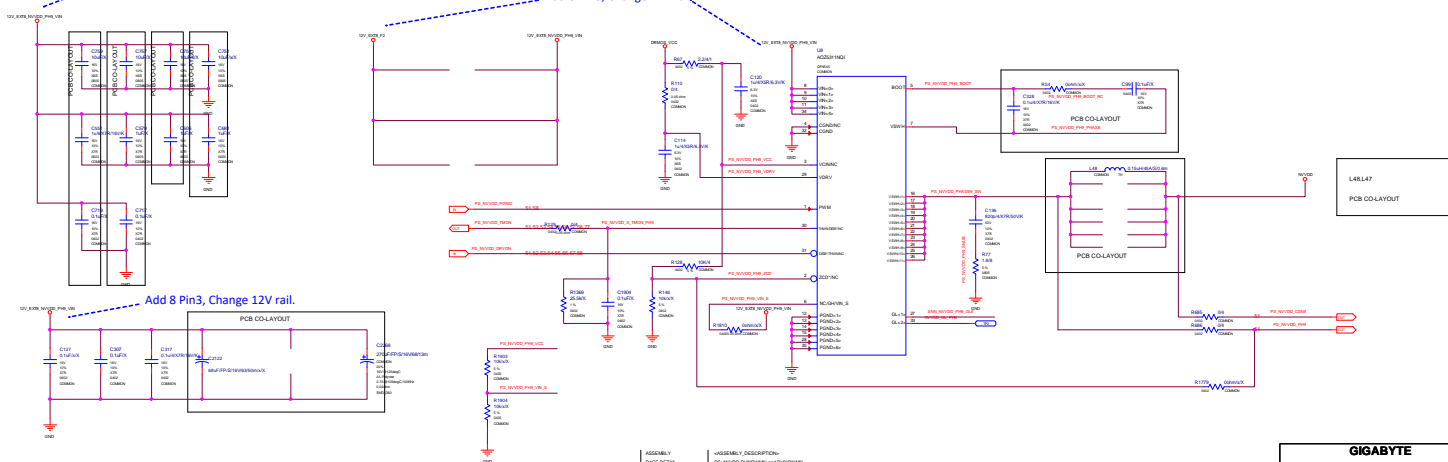
➤ Add 8 Pin3, Change 12V rail



Add 8 Pin3, Change 12V rail.

Add 8 Pin3, Change 12V rail.

– Add 8 Pin3, Change 12V rail



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ASSEMBLY
PAGE DETAIL

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PS; NAYCO PAB(PABE) and PAB(PABE)

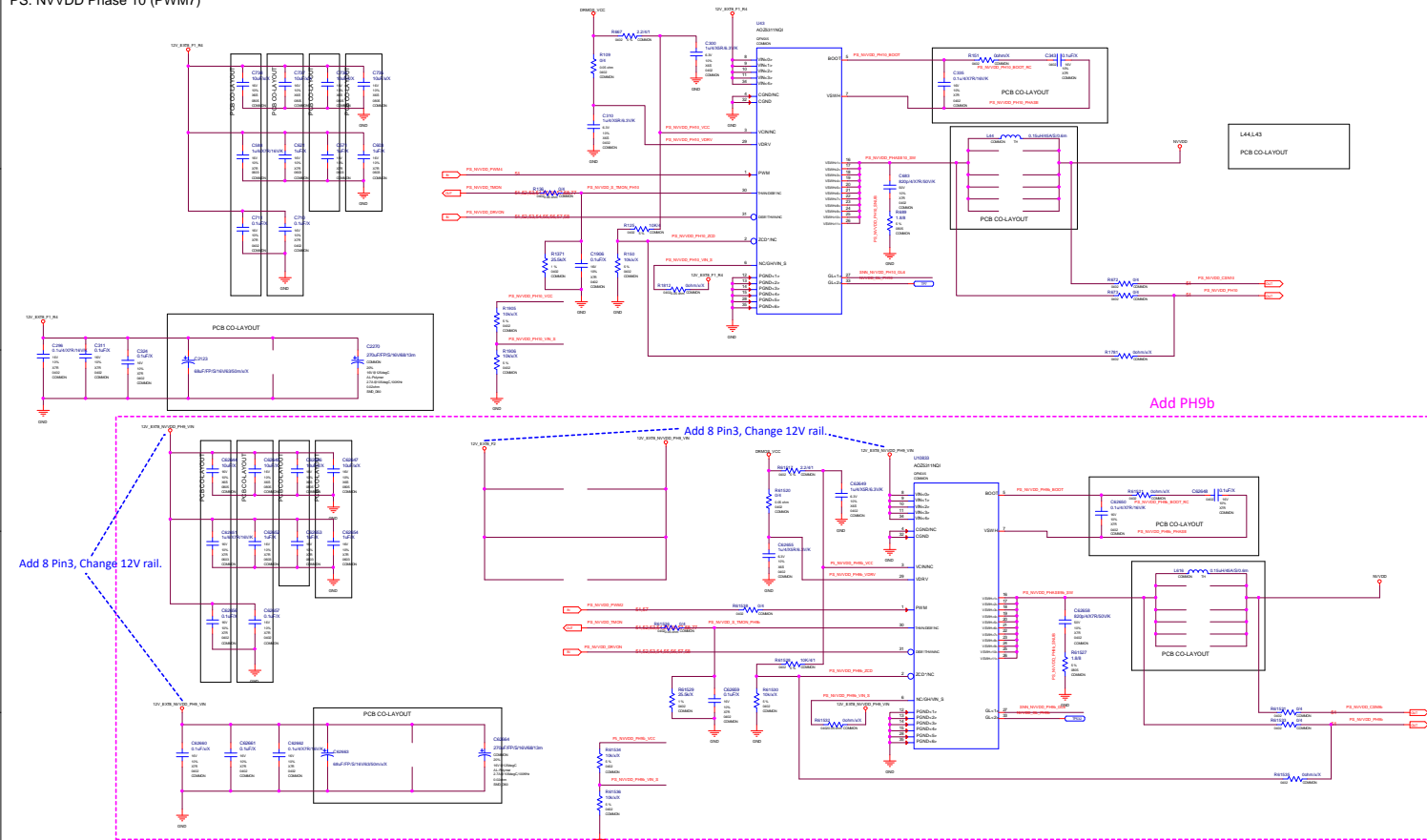
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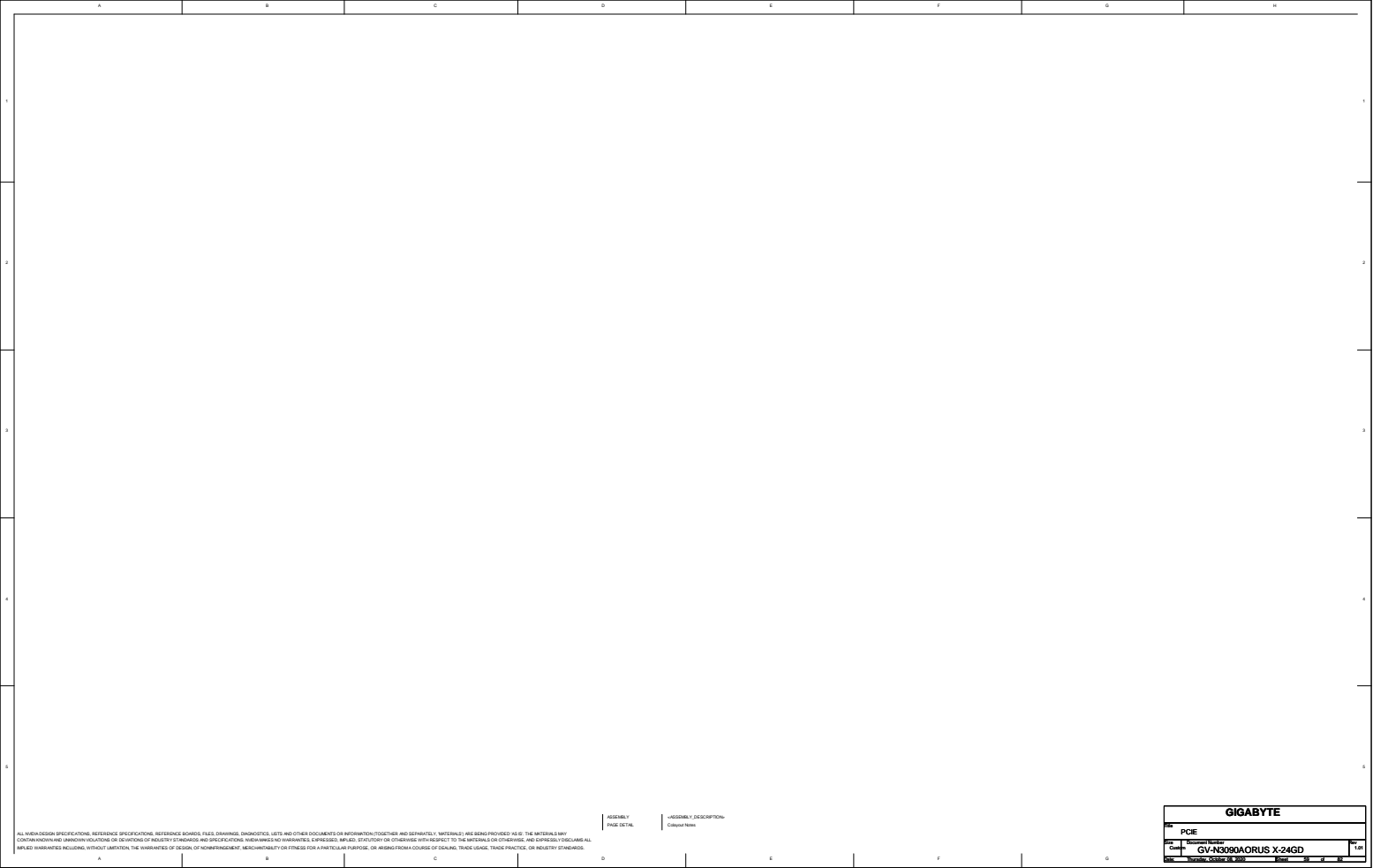
Document Number
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PS: NVVDD Phase 10 (PWM7)



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ASSEMBLY
PAGE 001

ASSEMBLY DESCRIPTION
Circuit Board

GIGABYTE			
PCIE			
GV-N3090AORUS X-24GD			
Rev 1.01			
Date: November 10, 2023			

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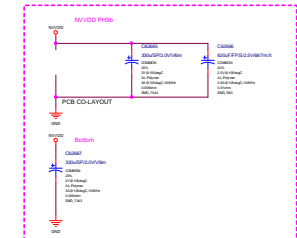
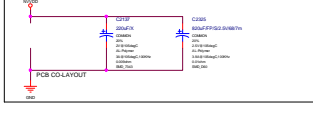
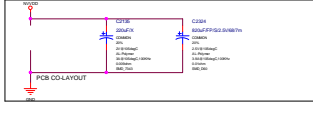
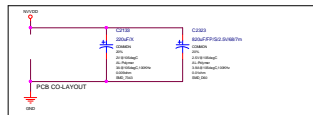
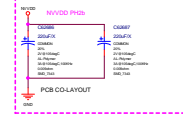
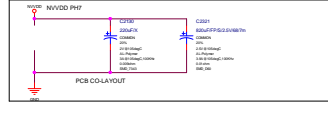
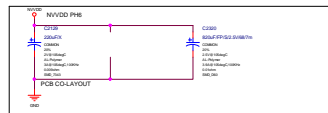
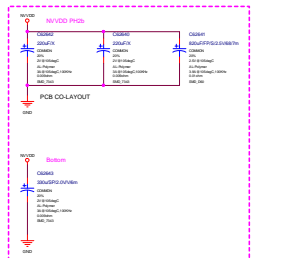
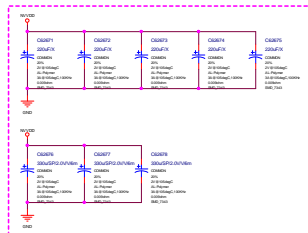
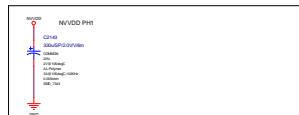
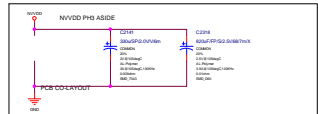
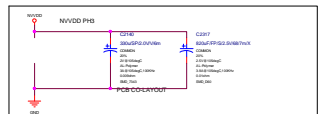
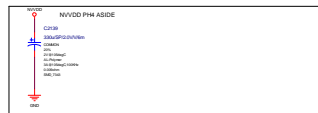
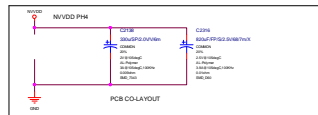
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ASSEMBLY
PAGE DETAIL

ASSEMBLY DESCRIPTION
Blank

GIGABYTE	
PCIE	
GV-N3090AORUS X-24GD	
Rev 1.01	

REFER TO PCB FOR BOM STUFFING



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PAGE DETAIL

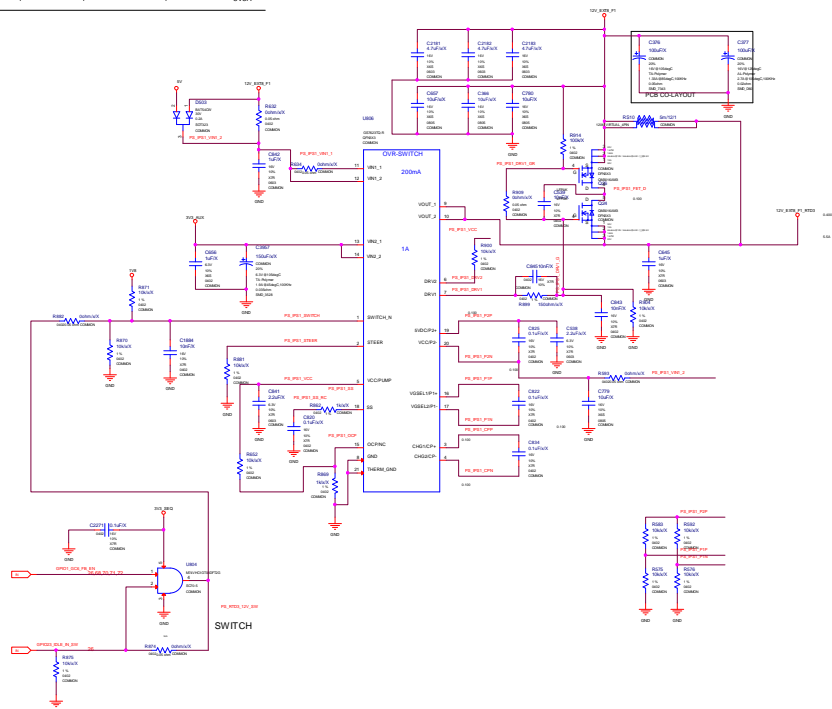
ASSEMBLY DESCRIPTION
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GIGABYTE	
PCIE	
GV-N3090AORUS X-24GD	
Rev 1.01	

PS: INPUT SWITCH RTD3

AND GATE LOGIC FOR P-BOARD

GPI01	GPI023	SWITCH	VOUT
0	0	0	12V_F
0	1	0	12V_F
1	0	0	12V_F
1	1	1	3V3A



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ASSEMBLY
PAGE DETAIL

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PS: INPUT SWITCH RTOS

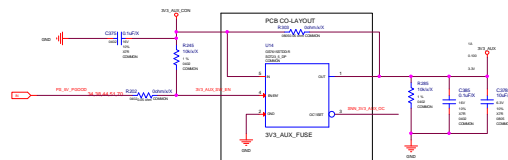
GIGABYTE

PCIE

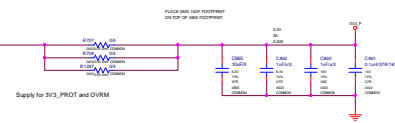
Document Number

GV-N3090AORUS X-24GL

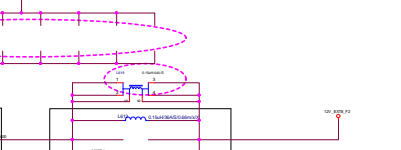
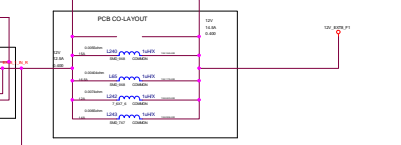
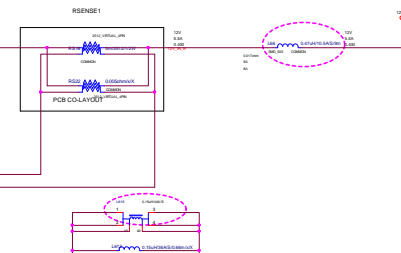
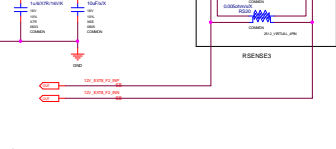
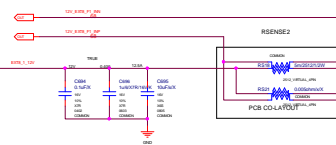
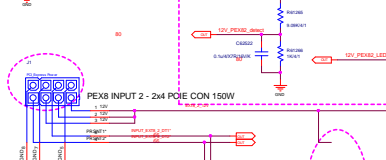
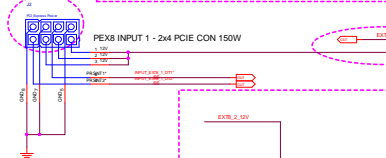
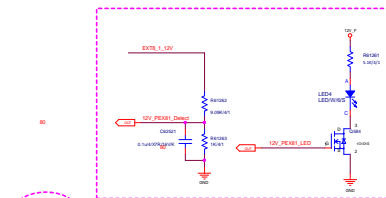
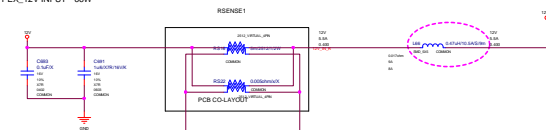
PS: Inputs, Filtering, and Monitoring



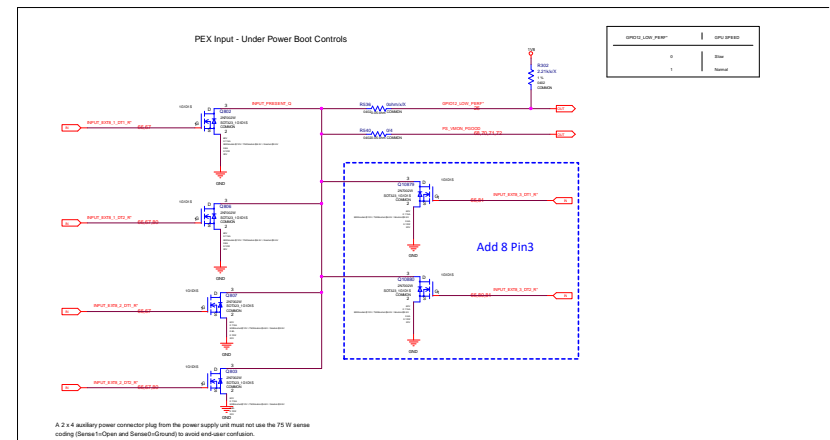
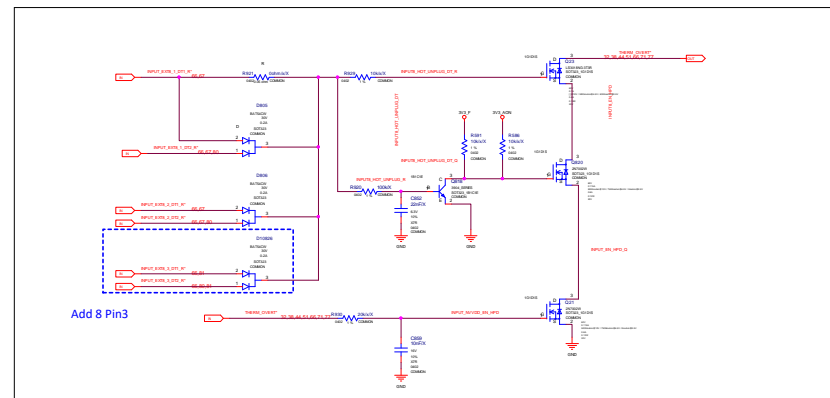
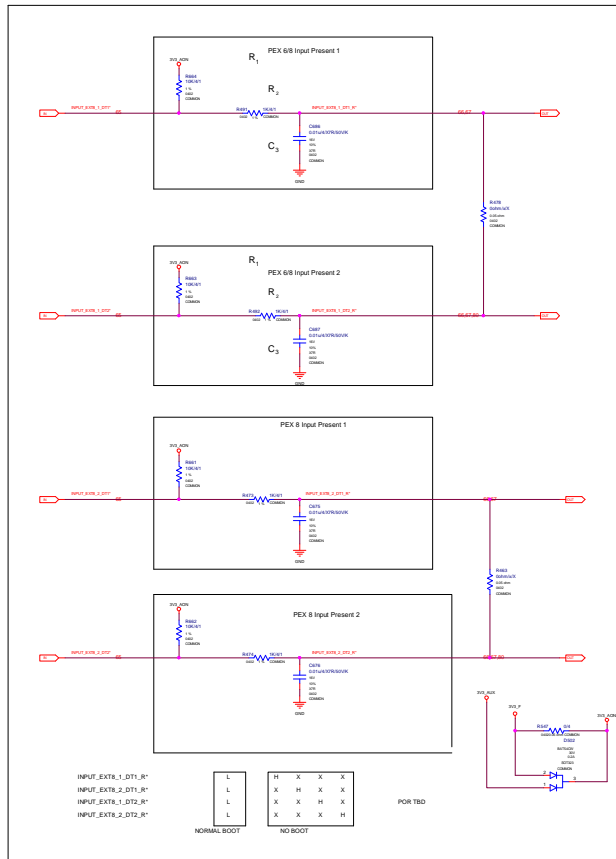
PEX 3V3 INPUT - 10W



PEX 12V INPUT - 66W



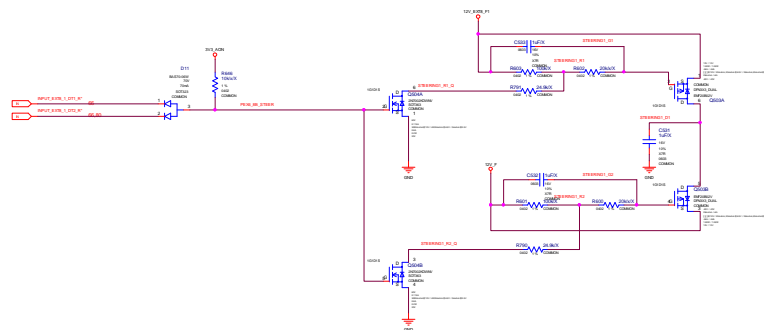
PS: 12V Current Steering & Hot Unplug Detect



PS: Discrete Steering

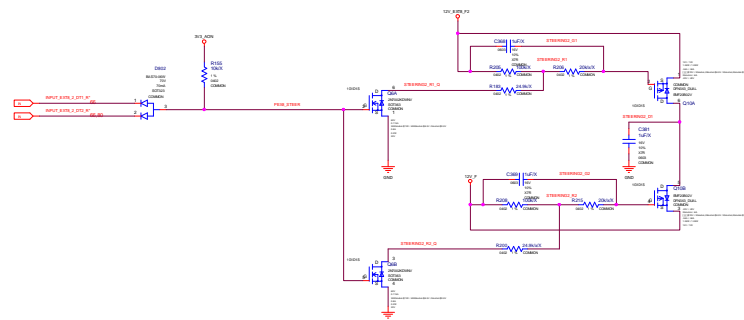
12V CURRENT STEERING (UNDER POWER BOOT):

PEX12V AND 12V EXT8 F1



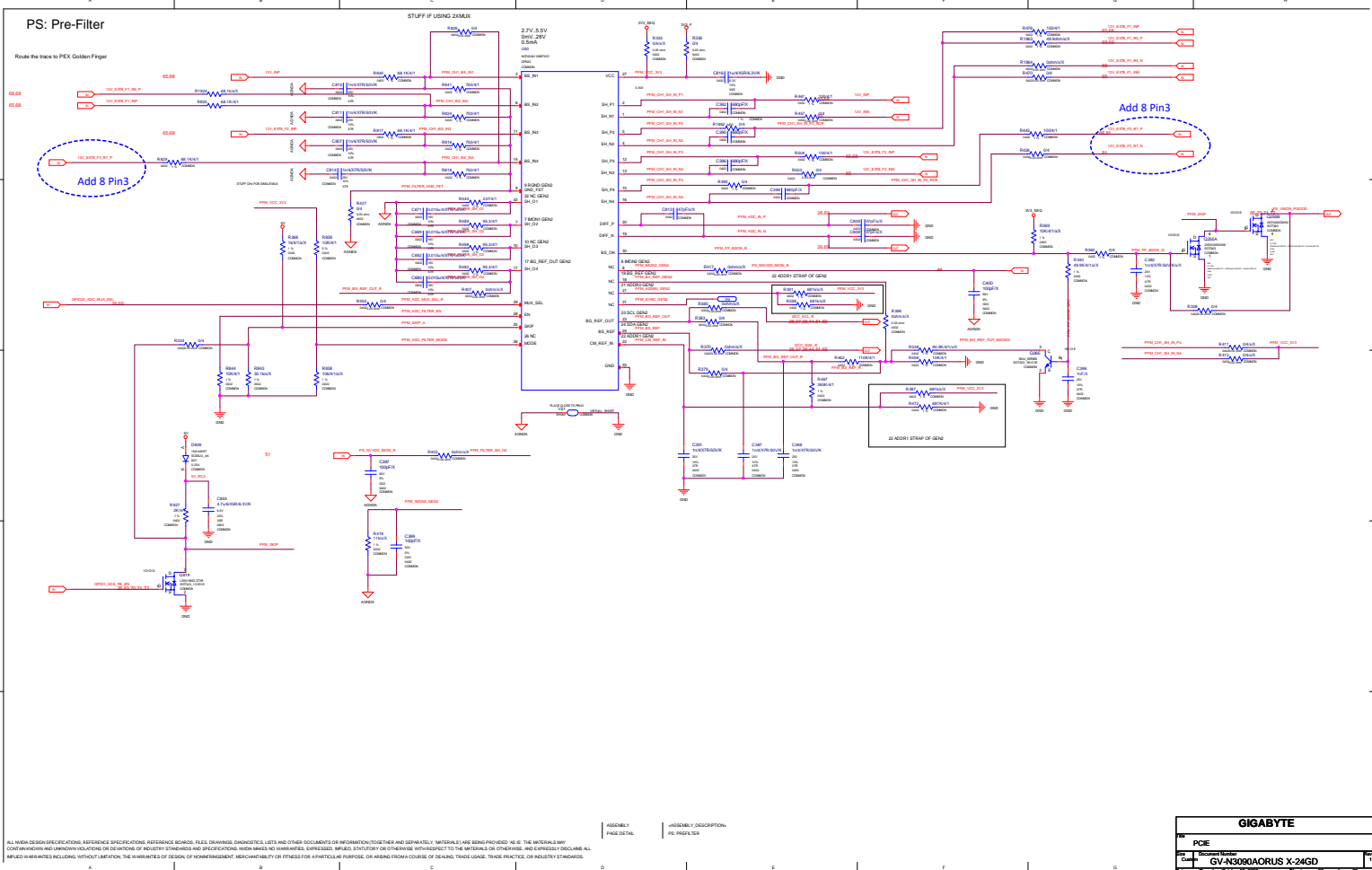
NO STUFF BY DEFAULT REF ONLY

PEX12V AND 12V_EXT8_F2



Route the trace to PEX Golden Fingers

Route the trace to PEX Golden Fingers



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ASSEMBLY
PAGE DETAIL

ASSEMBLY_DESCRIPTION
PS: PREFIXER

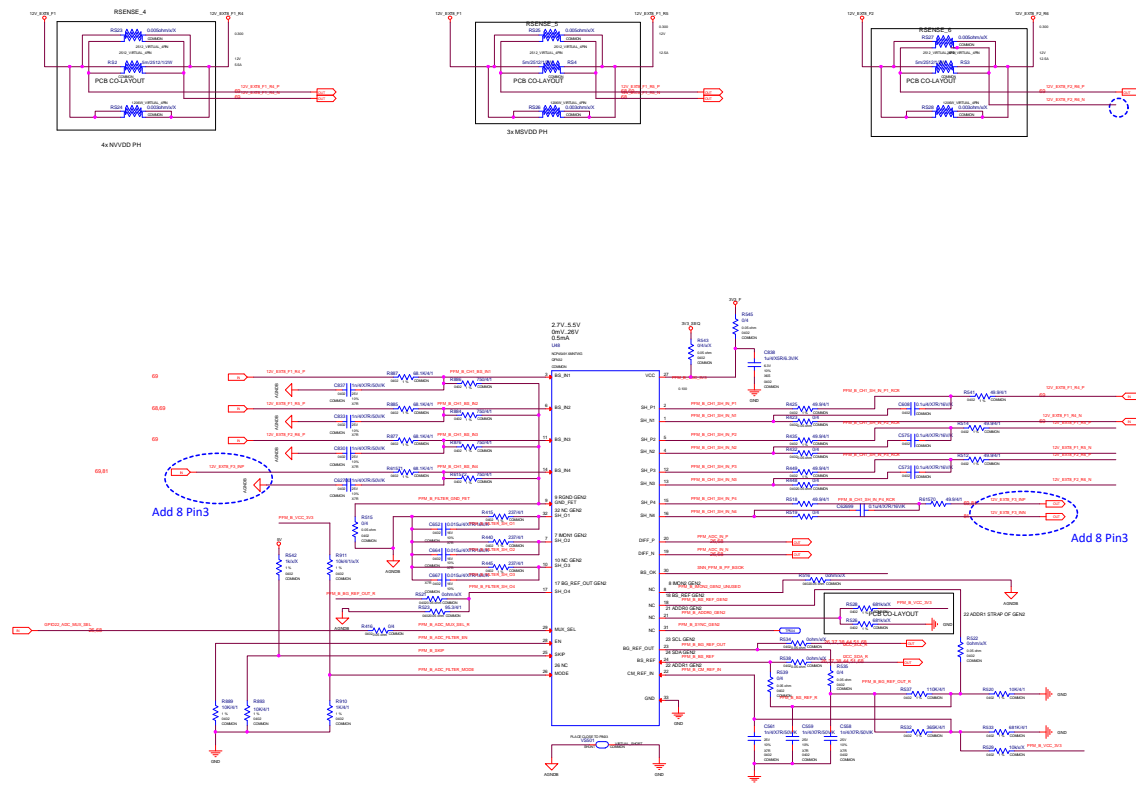
GIGABYTE

PC

Document Number
GV-N3090AORUS X-24G

1

PS: Pre-Filter B

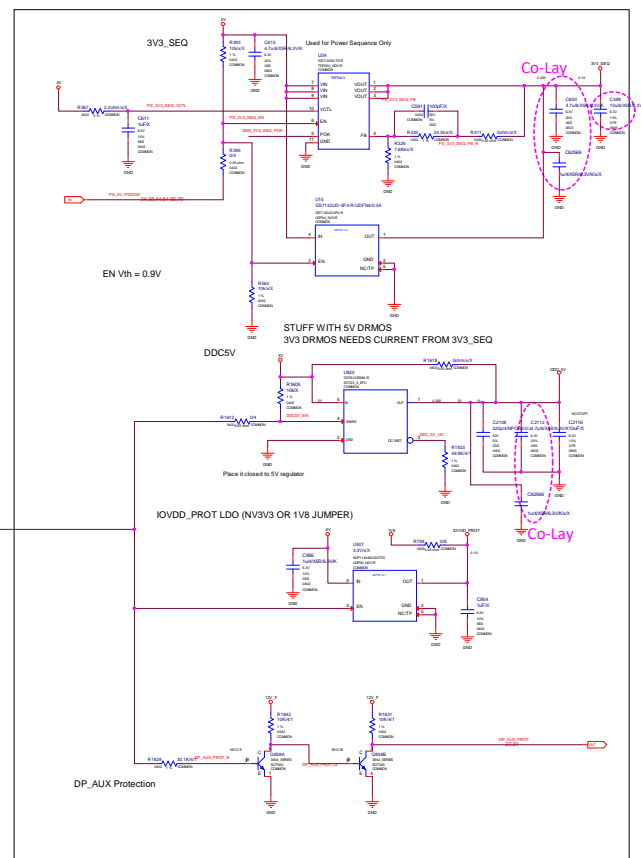
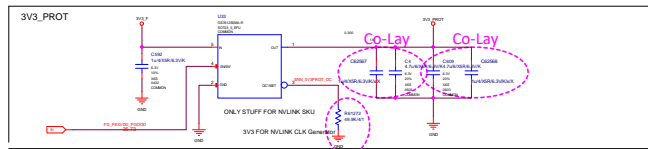
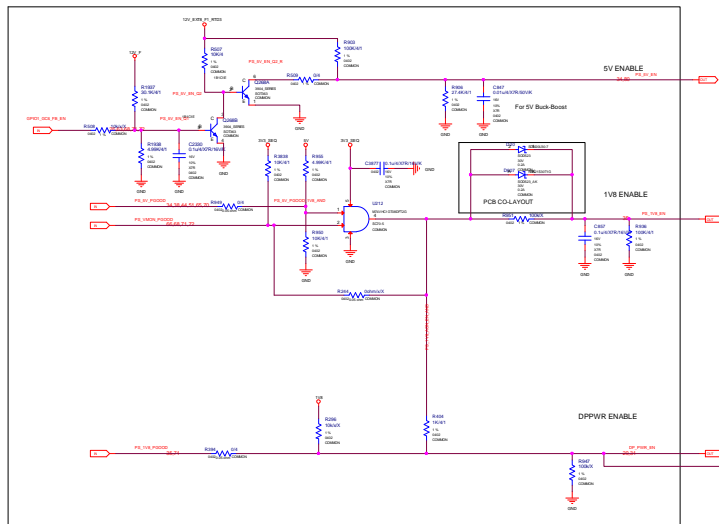


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ASSEMBLY PAGE DETAIL
ASSEMBLY DESCRIPTION: PG: PRE-Filter B

GIGABYTE	
PCB	Model: GV-N3080AURUS X-24GD
Date: 2024.03.28	Ver: 1.00

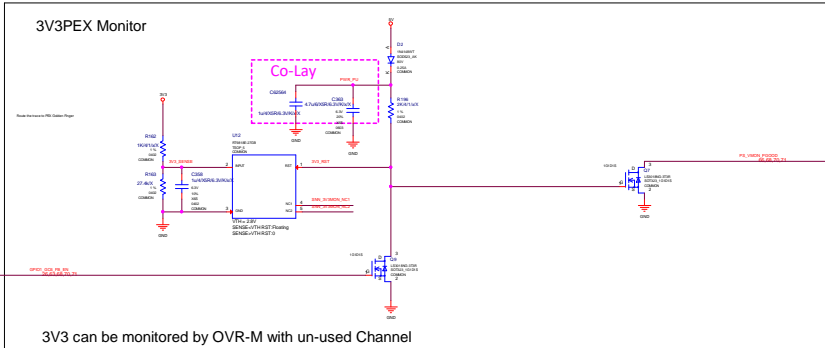
SEQUENCE:5V,1V8,3V3_SEQ ENABLE



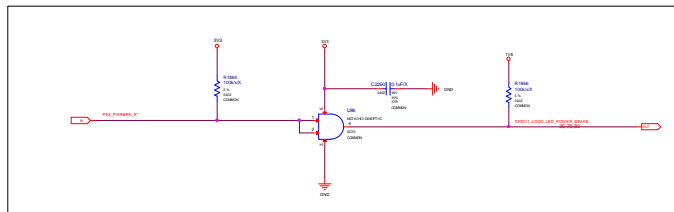
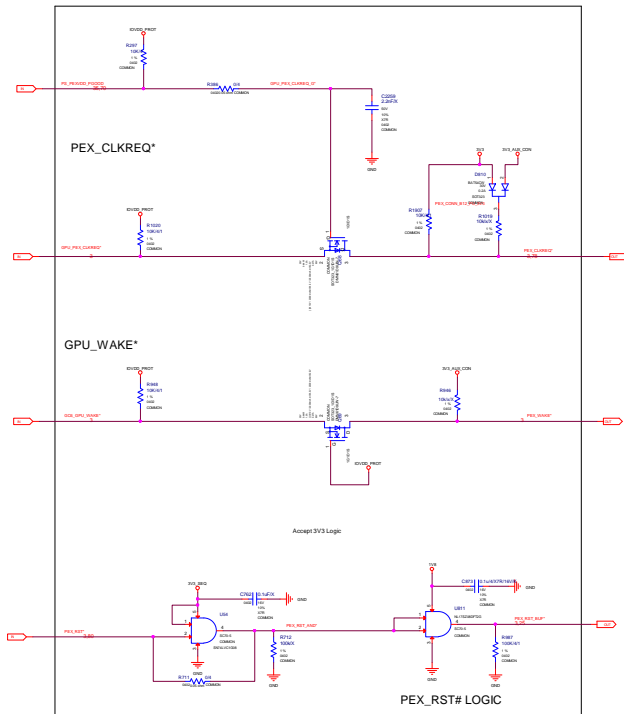
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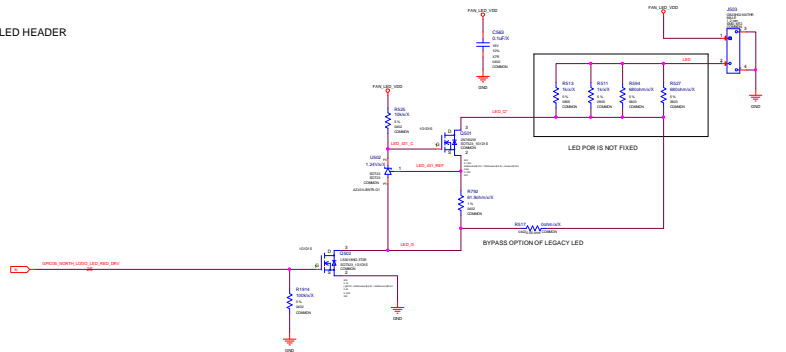
ASSEMBLY DESCRIPTION
Sequence No. PEX, FREN



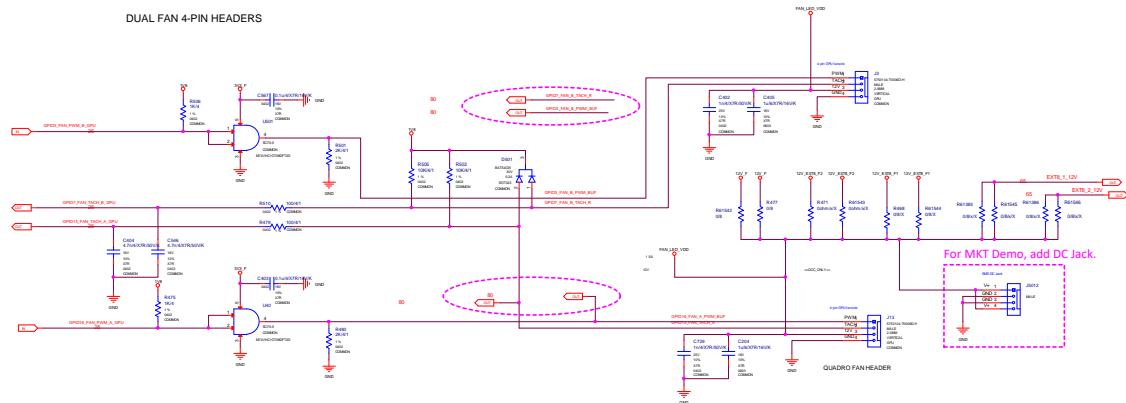
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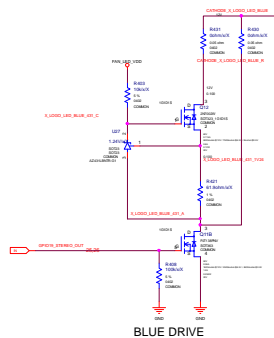
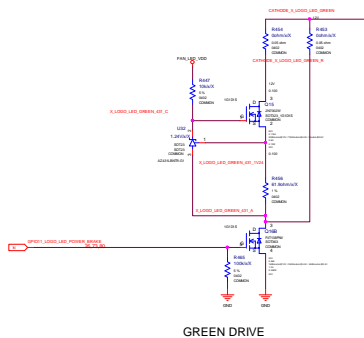
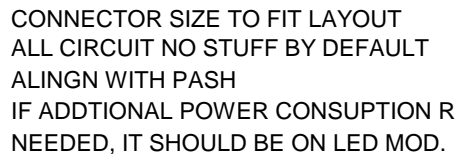


2-PIN LED HEADER



DUAL FAN 4-PIN HEADERS





Mechanical: Mounting holes

BK up



BK low



GPU



MOS



Backplane



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ASSEMBLY
PHASE DETAIL
MECH1

GIGABYTE	
PCIE	
GV-N3080AORUS X-24GD	
Model	GV-N3080AORUS X-24GD
Rev	1.00

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PCI TERM

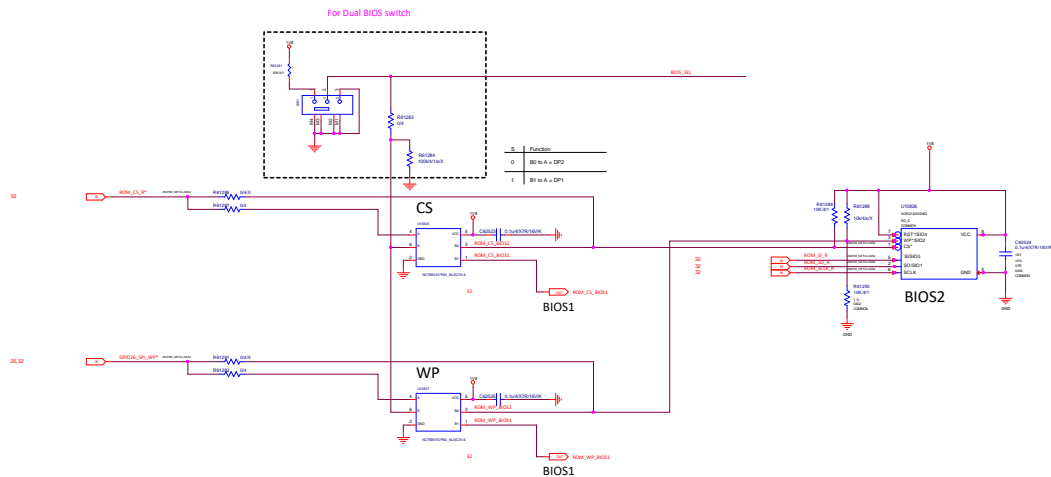


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ASSEMBLY
PAGE DETAIL

	ASSEMBLY DESCRIPTION
PCIE TERM	

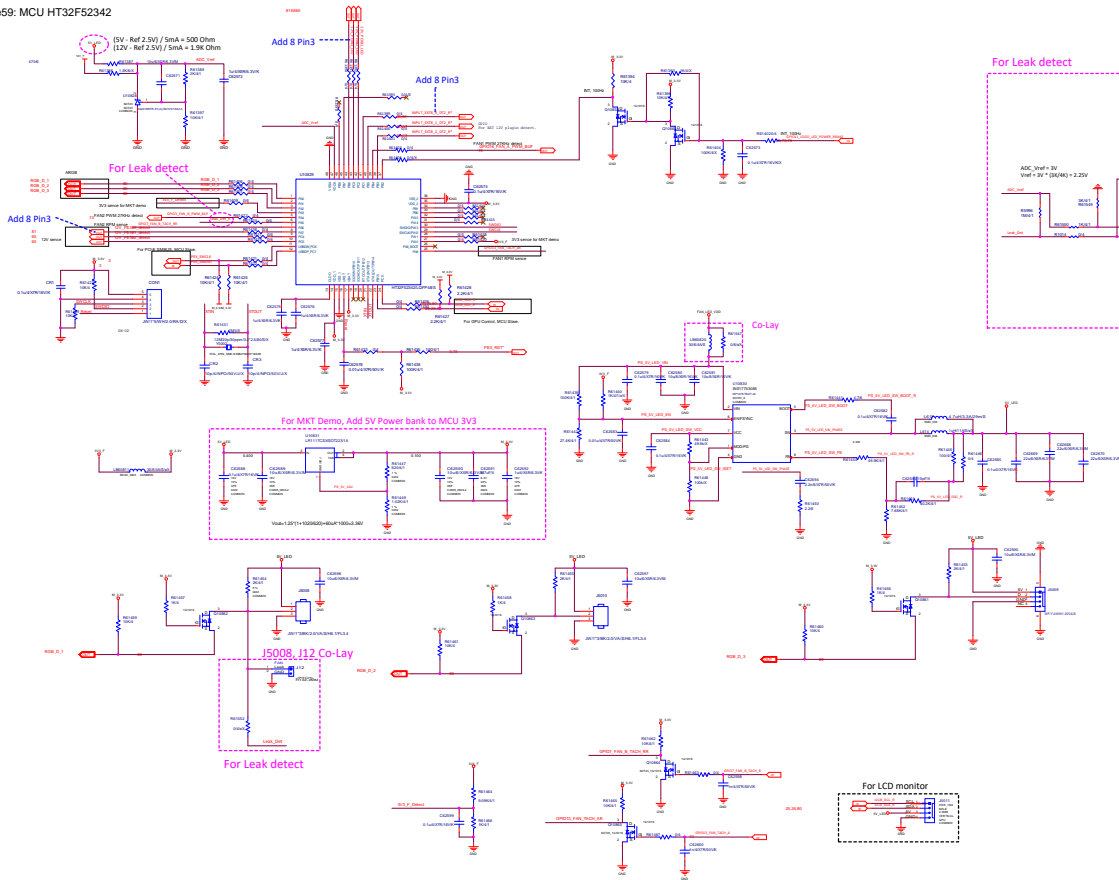
BIOS Switch

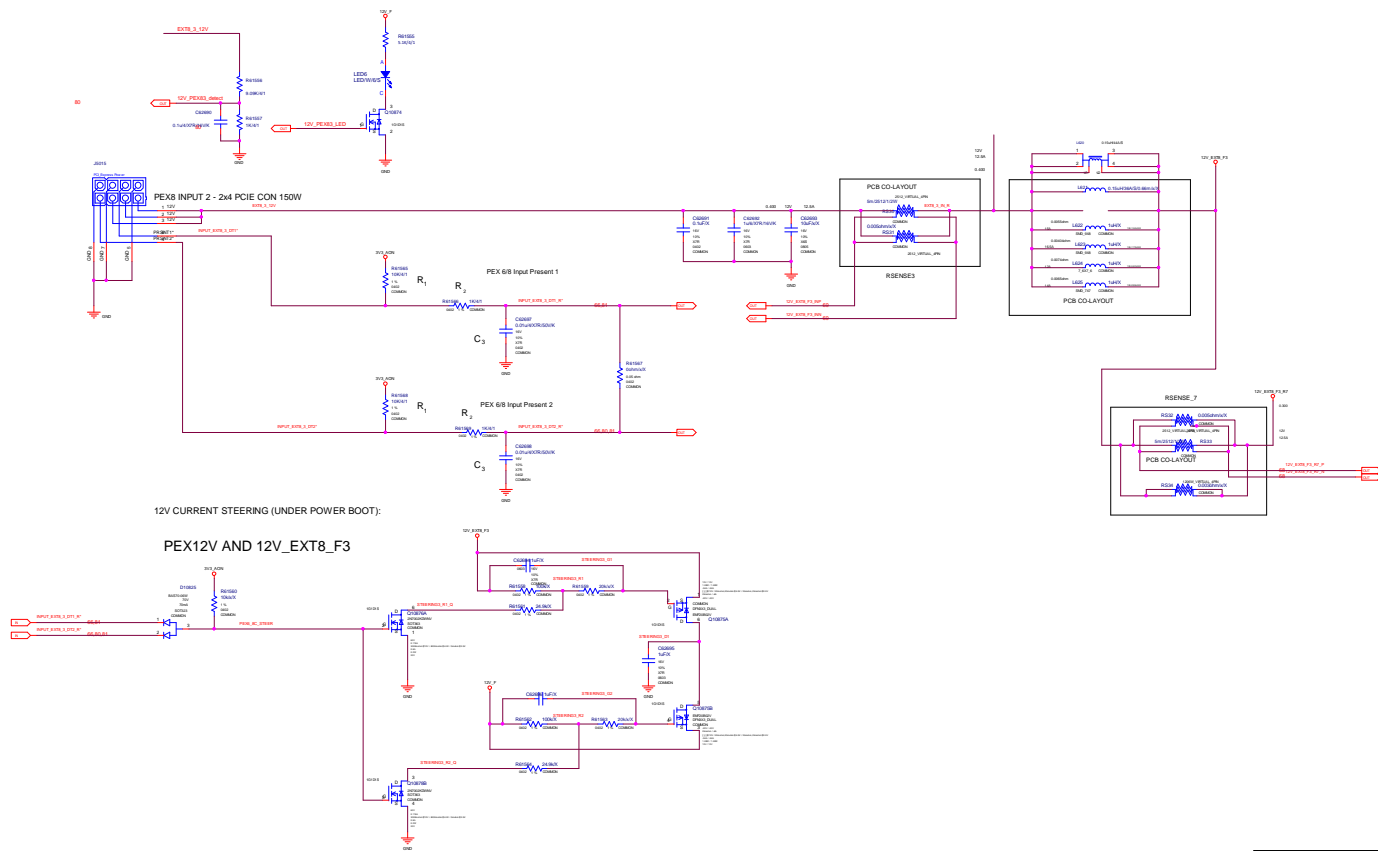


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ASSEMBLY
PAGE DETAIL

MCU ZERO POWER IDLE





History

Date	Modify
2018.07.21	First version